

FIG. 1

```

XPPPPreloadConfig(_XppCfg_foo );
for(int i=0; i<1000; ++i) {
    XPPPPreload( 2, &a[i*30], 30 );
    XPPPPreload( 0, &b[i*200], 200 );
    XPPPPreloadClean( 5, &c[i*10], 10 );
    XPPPExecute( );
}
/*
    Other RISC computations ...
    In the meanwhile the burst preloads and
    the previous configuration are running;
    The new configuration is executed as soon
    as the preloads and the previous
    configuration are finished.
    New burst preloads can be issued
    according to the FIFO length.
*/
    
```

Note: In all Places where Constants are Used,
 the Value should Actually come from a
 Register

Legend:

Per Thread State Resource
Volatile (non-state) Resource Write Back if Dirty
Volatile Read only Resource

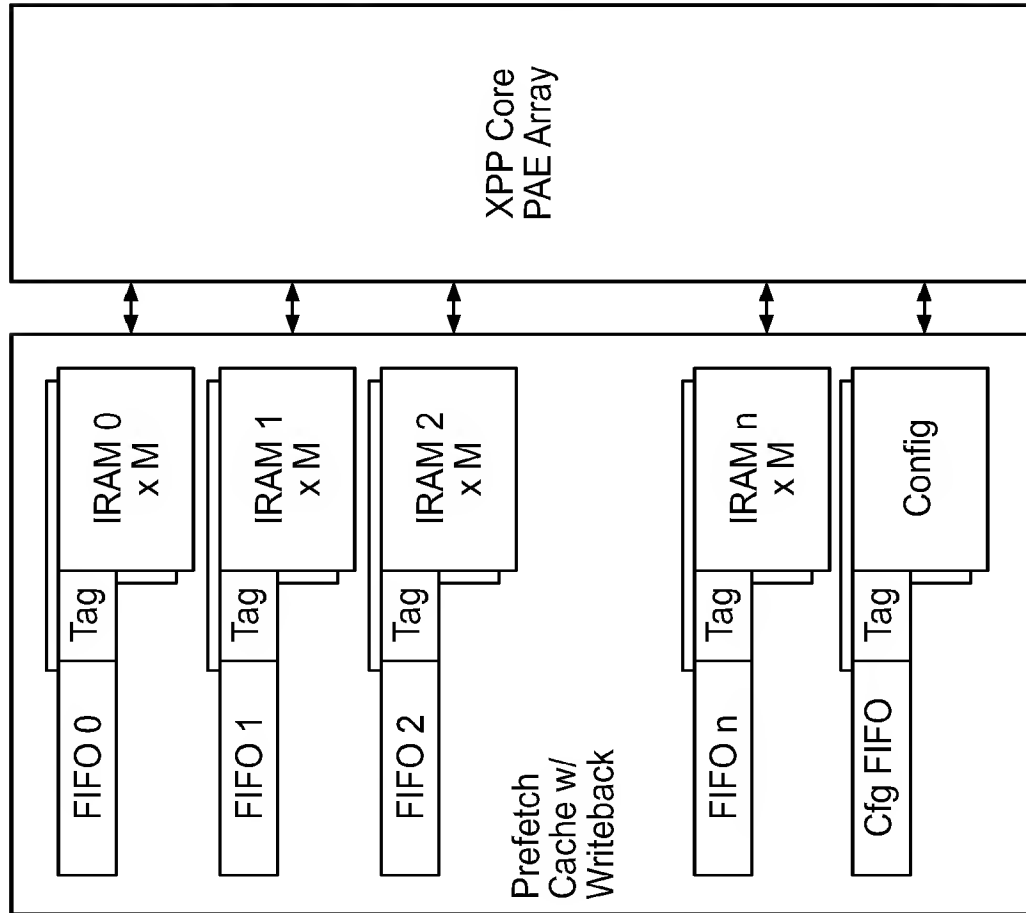


FIG. 2

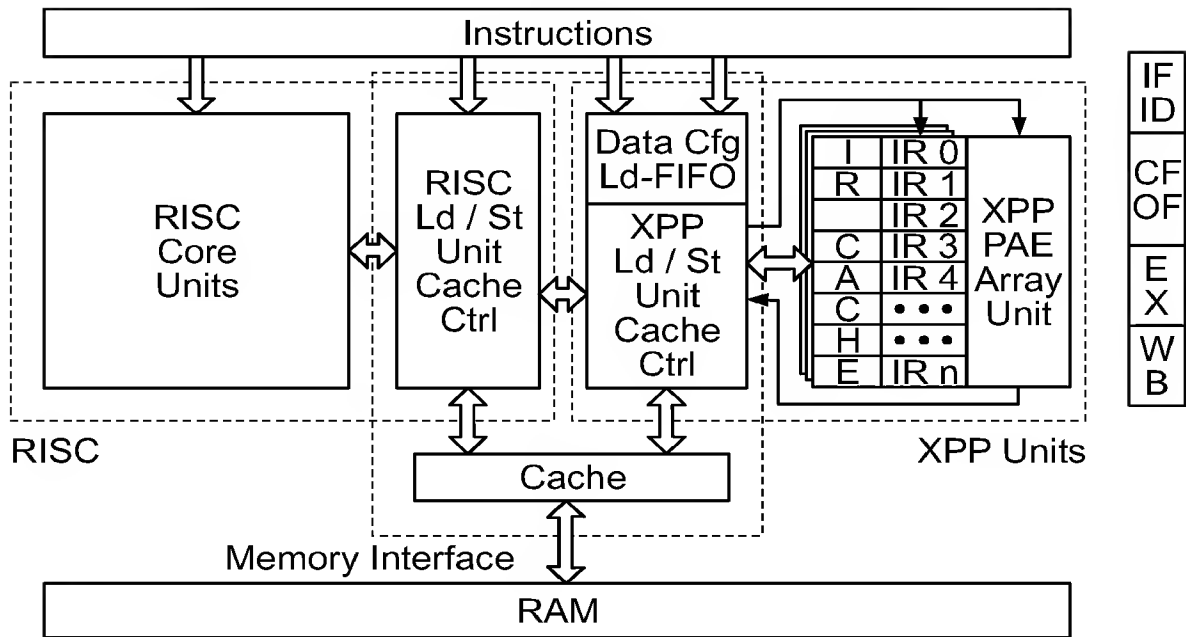


FIG. 3

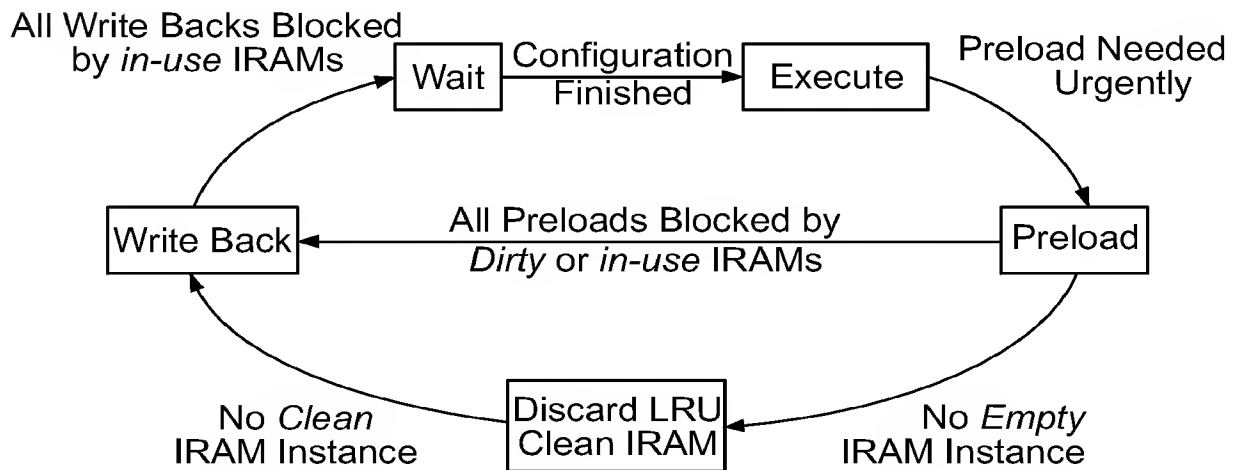


FIG. 4

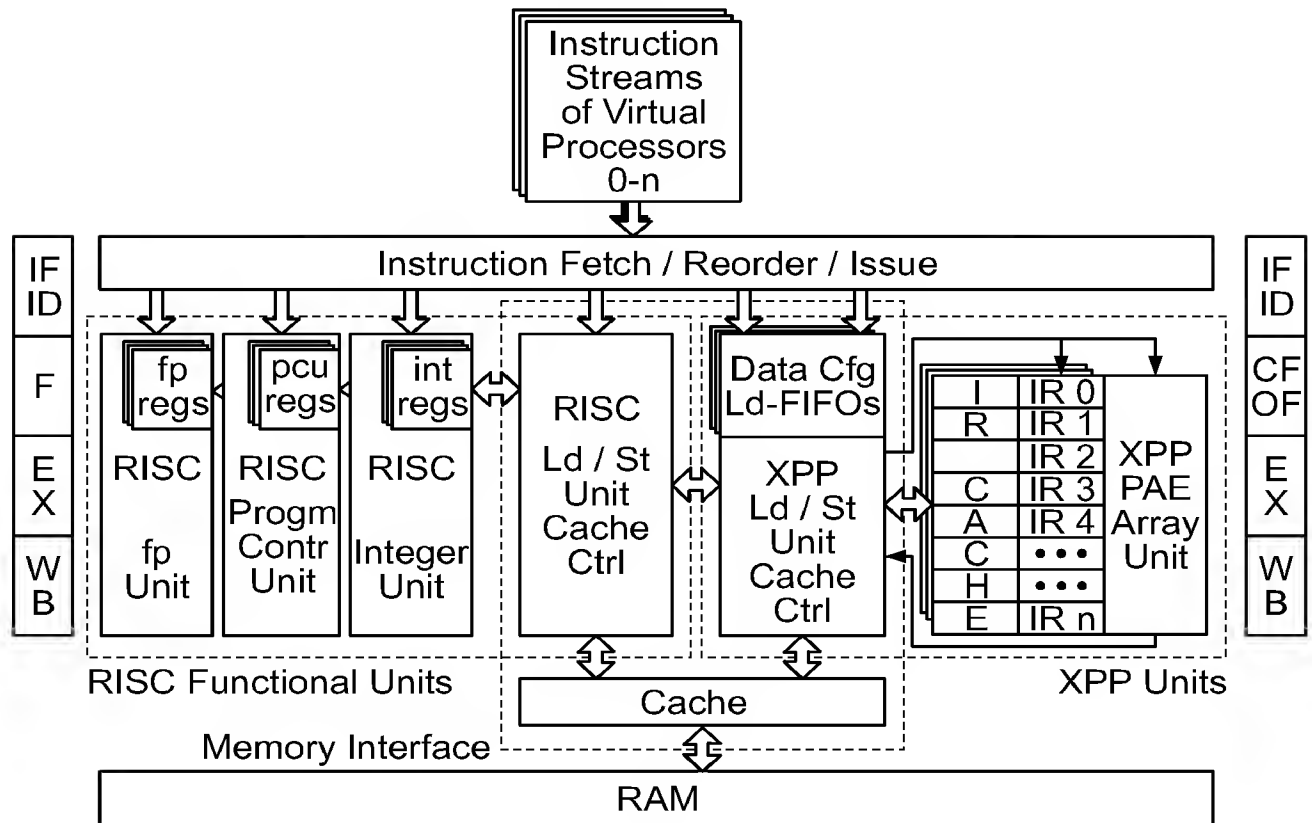


FIG. 5

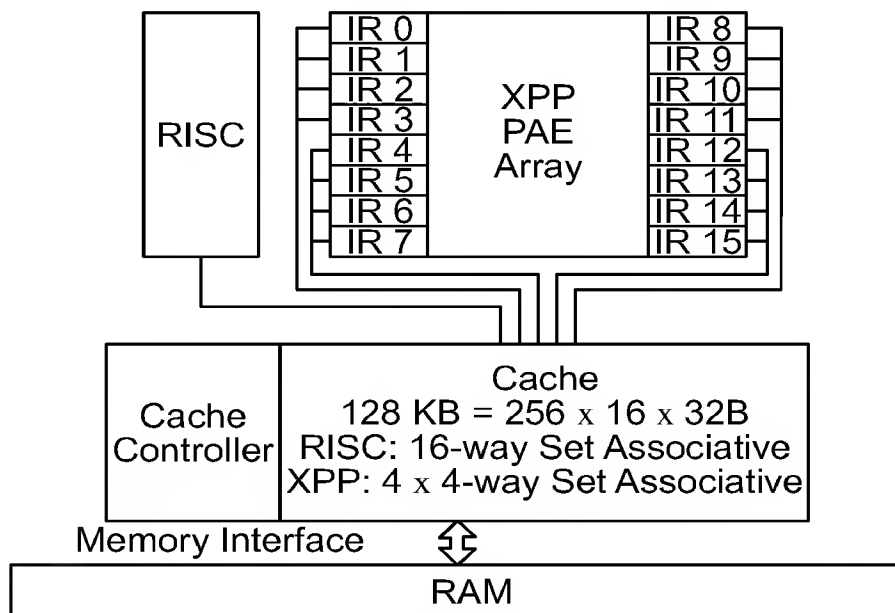


FIG. 6

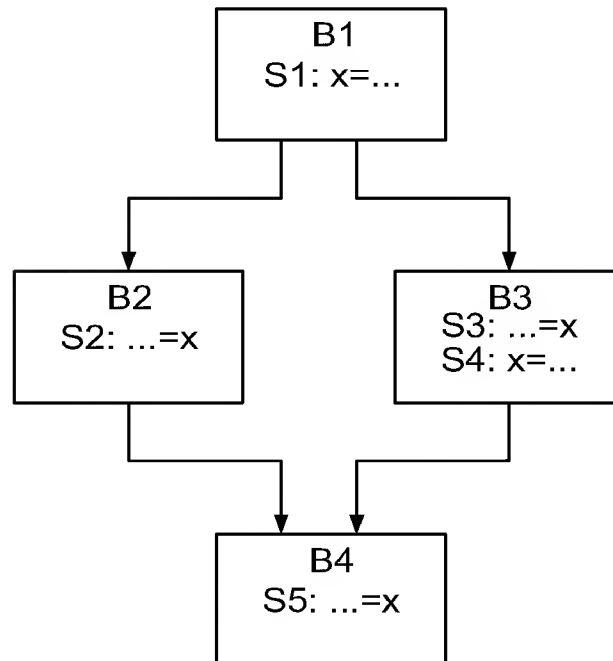


FIG. 7

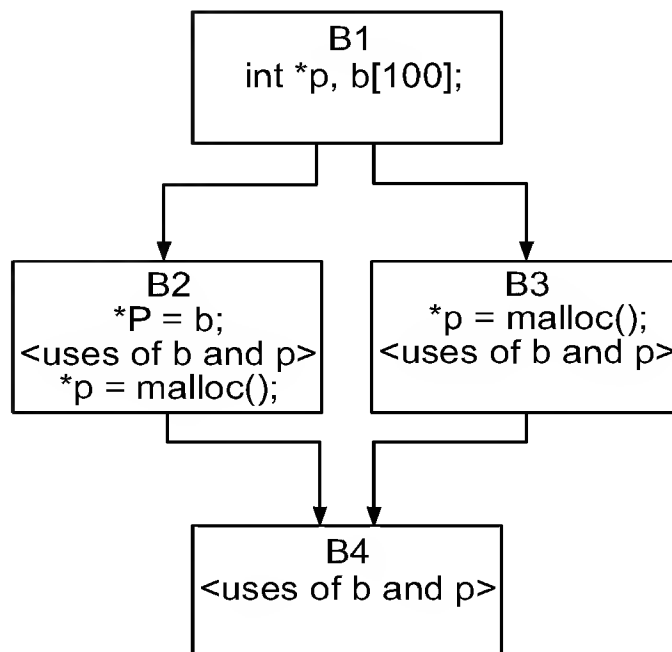


FIG. 8

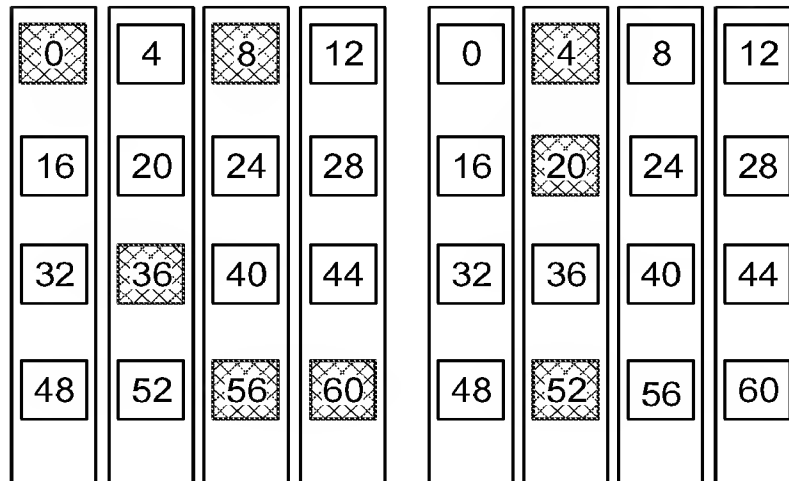


FIG. 9

```

for (j=1; j<=N-1; i++)
  for (j=1; j<=N; j++)
    b[i] [j] = 0.25 * (a[i-1] [j] + a[i] [j-1] +
a[i+1] [j] + a[i] [j+1]);

```



FIG. 10

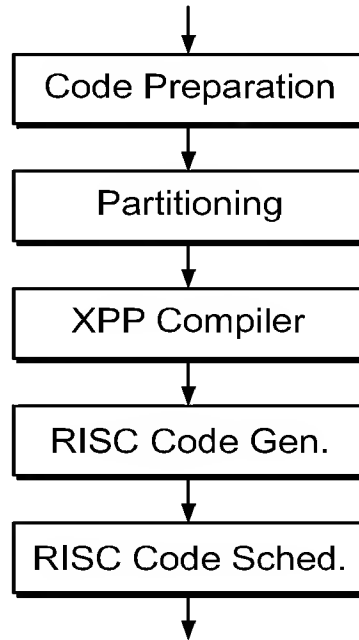


FIG. 11

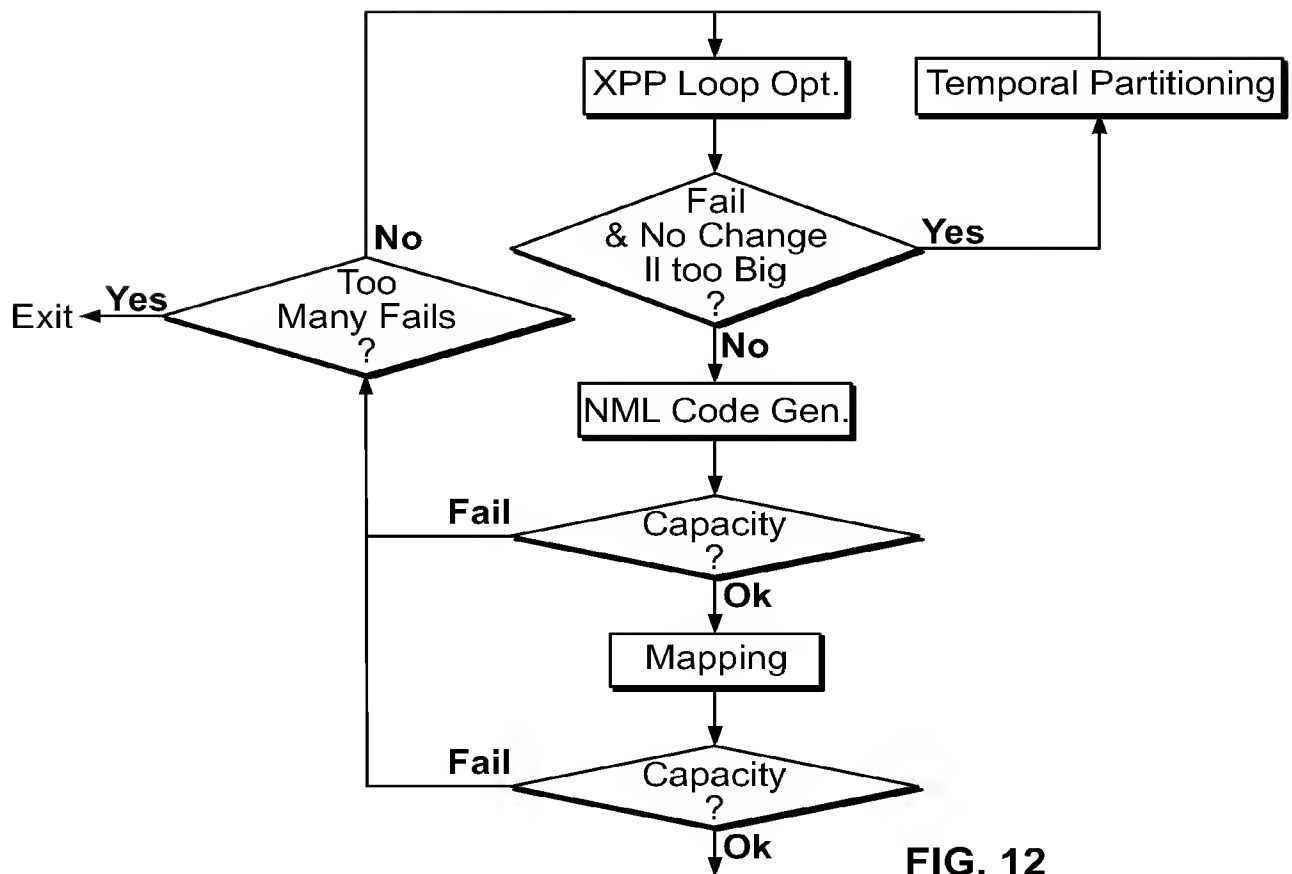


FIG. 12

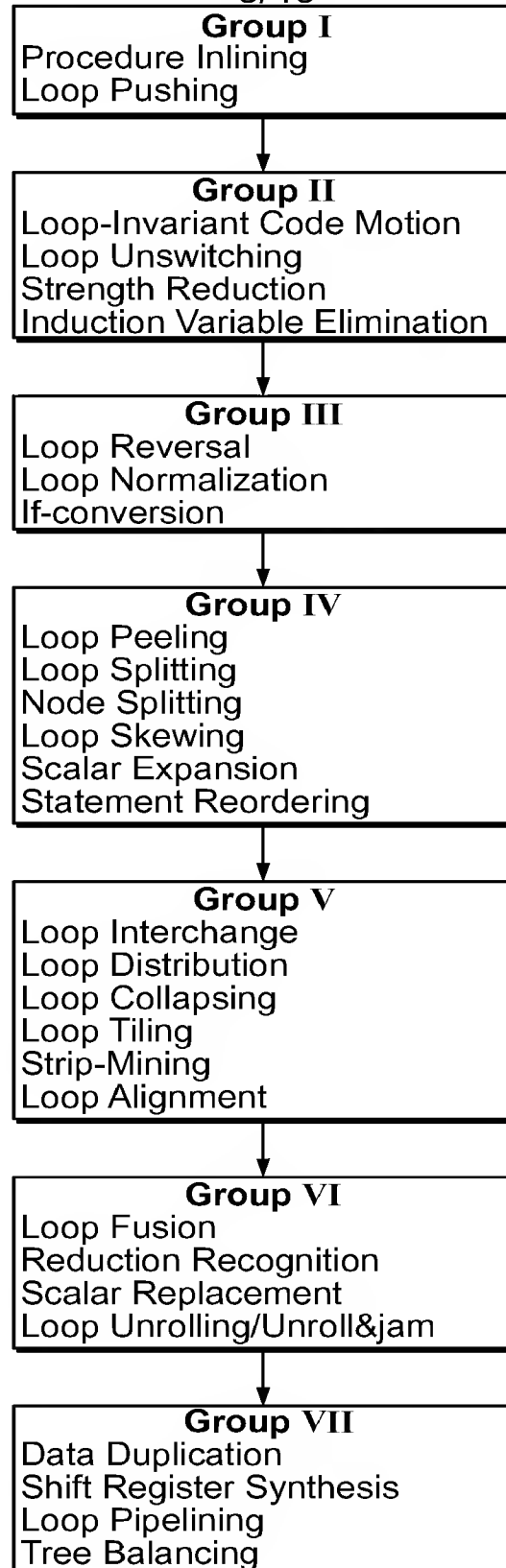


FIG. 13

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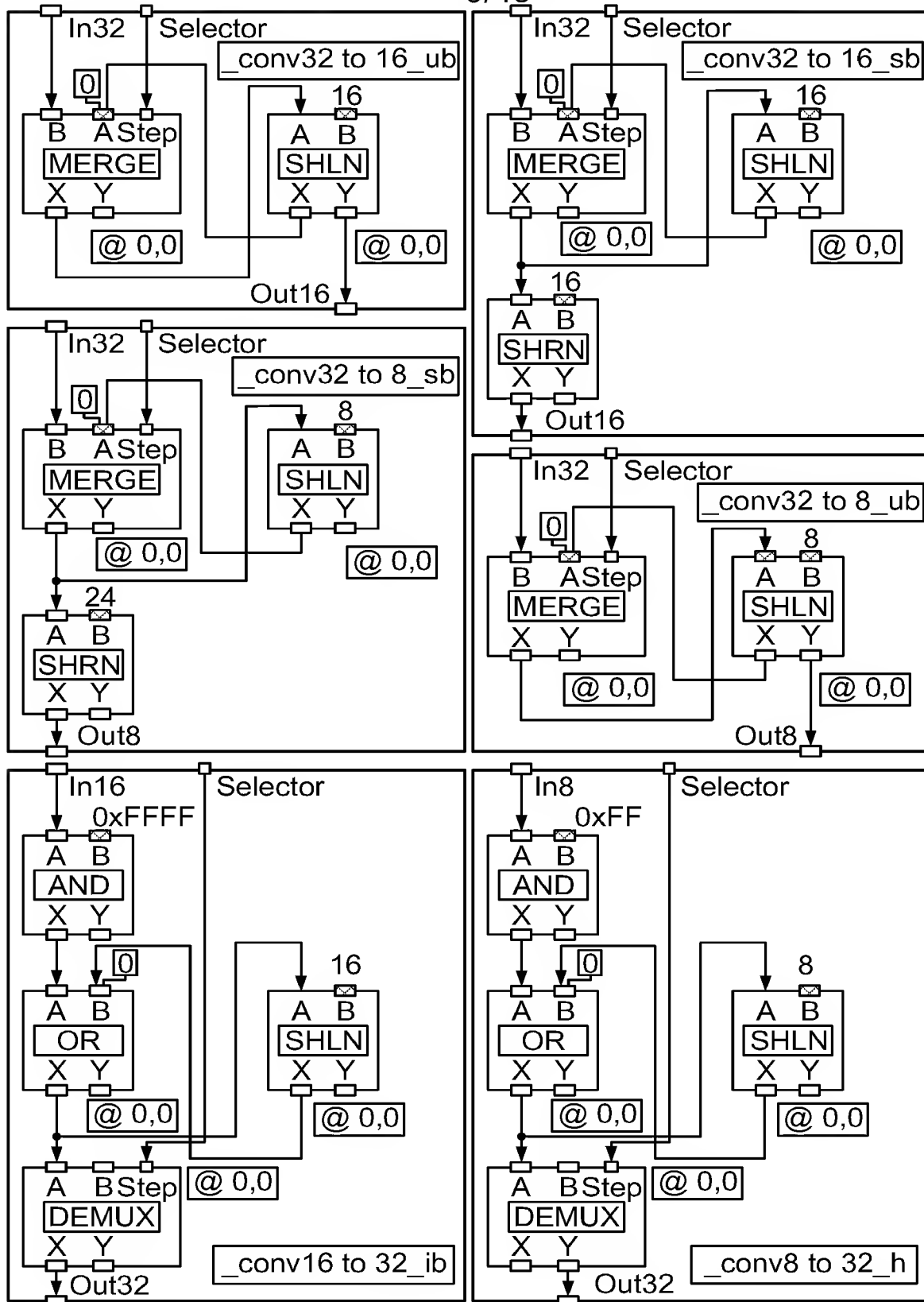


FIG. 14

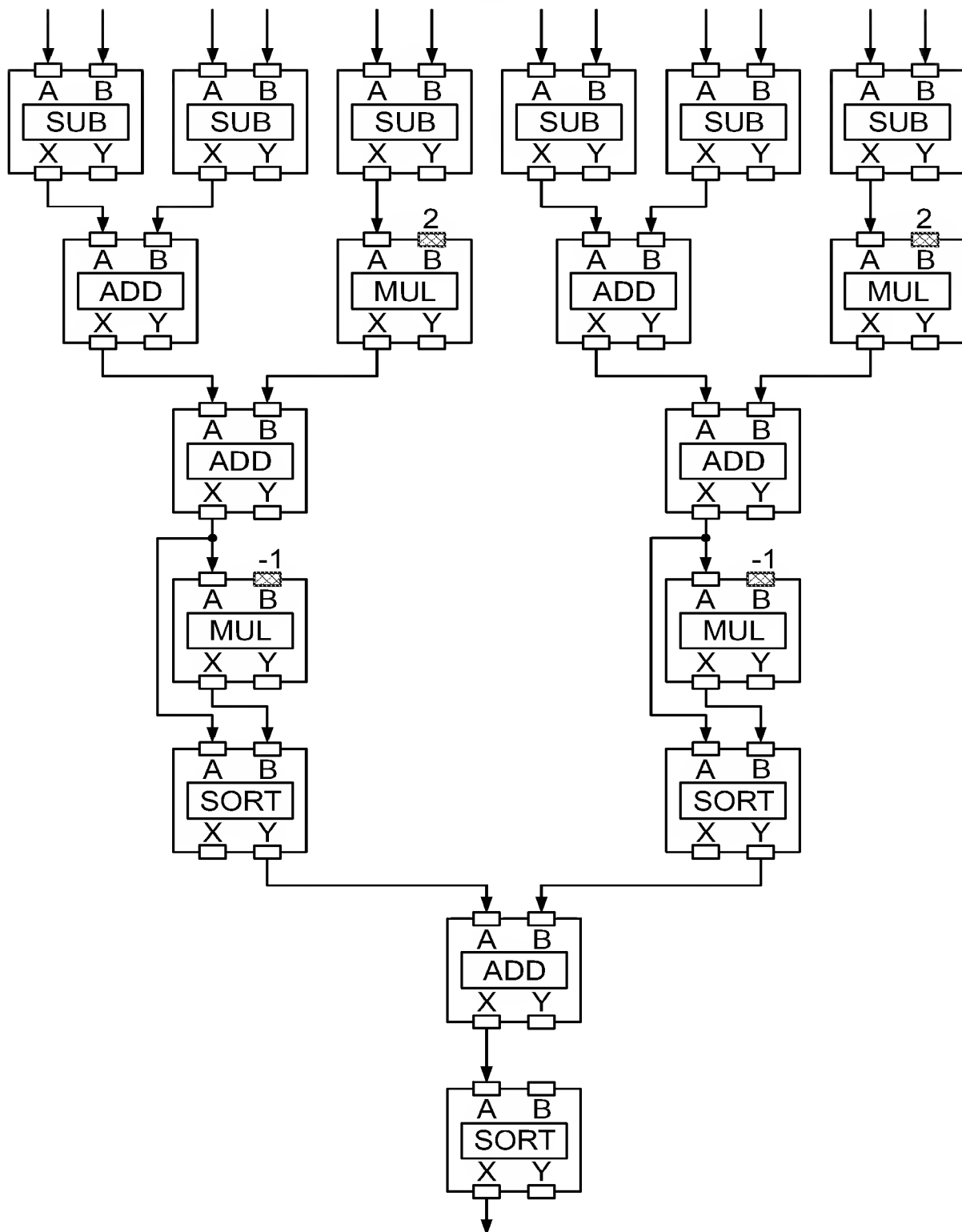


FIG. 15

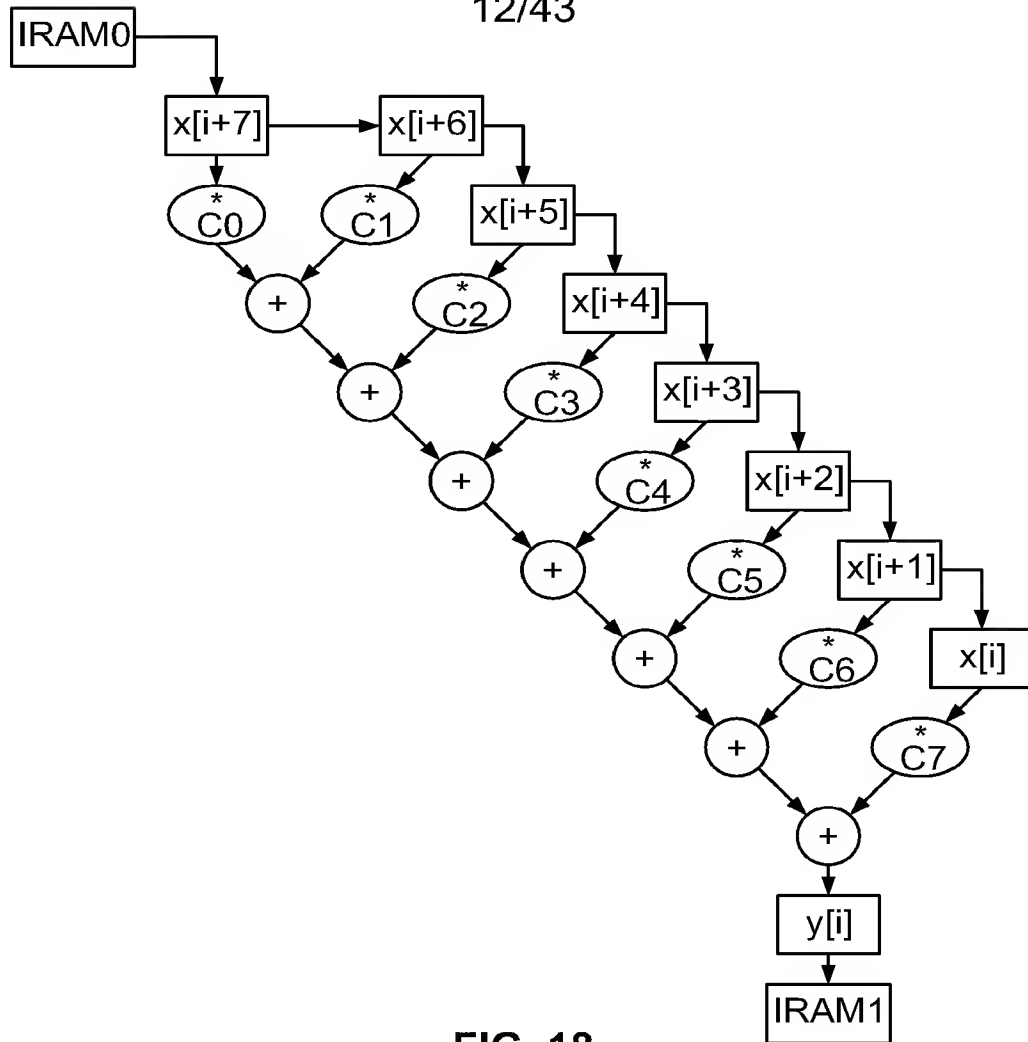


FIG. 18

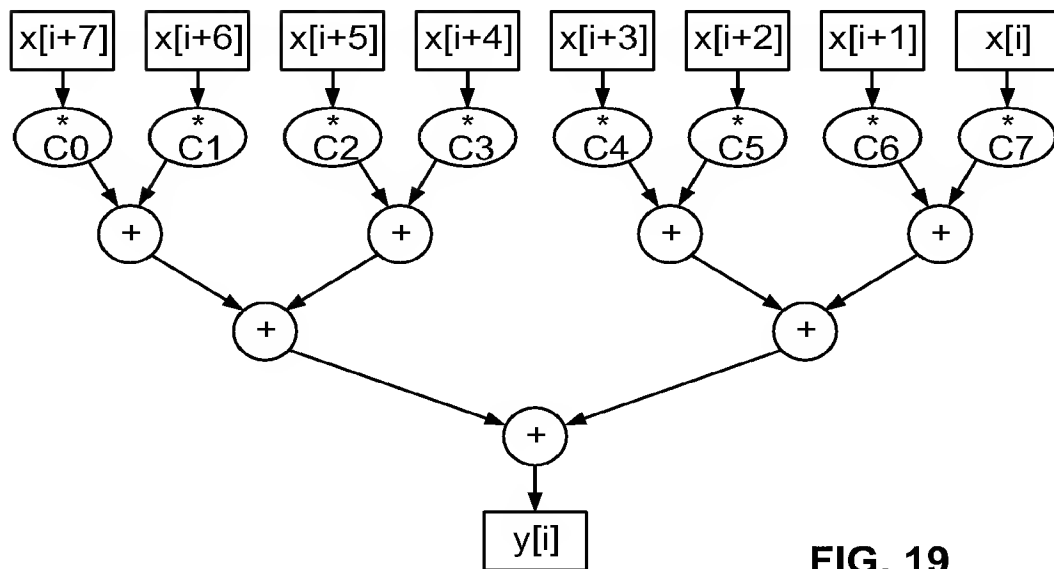


FIG. 19

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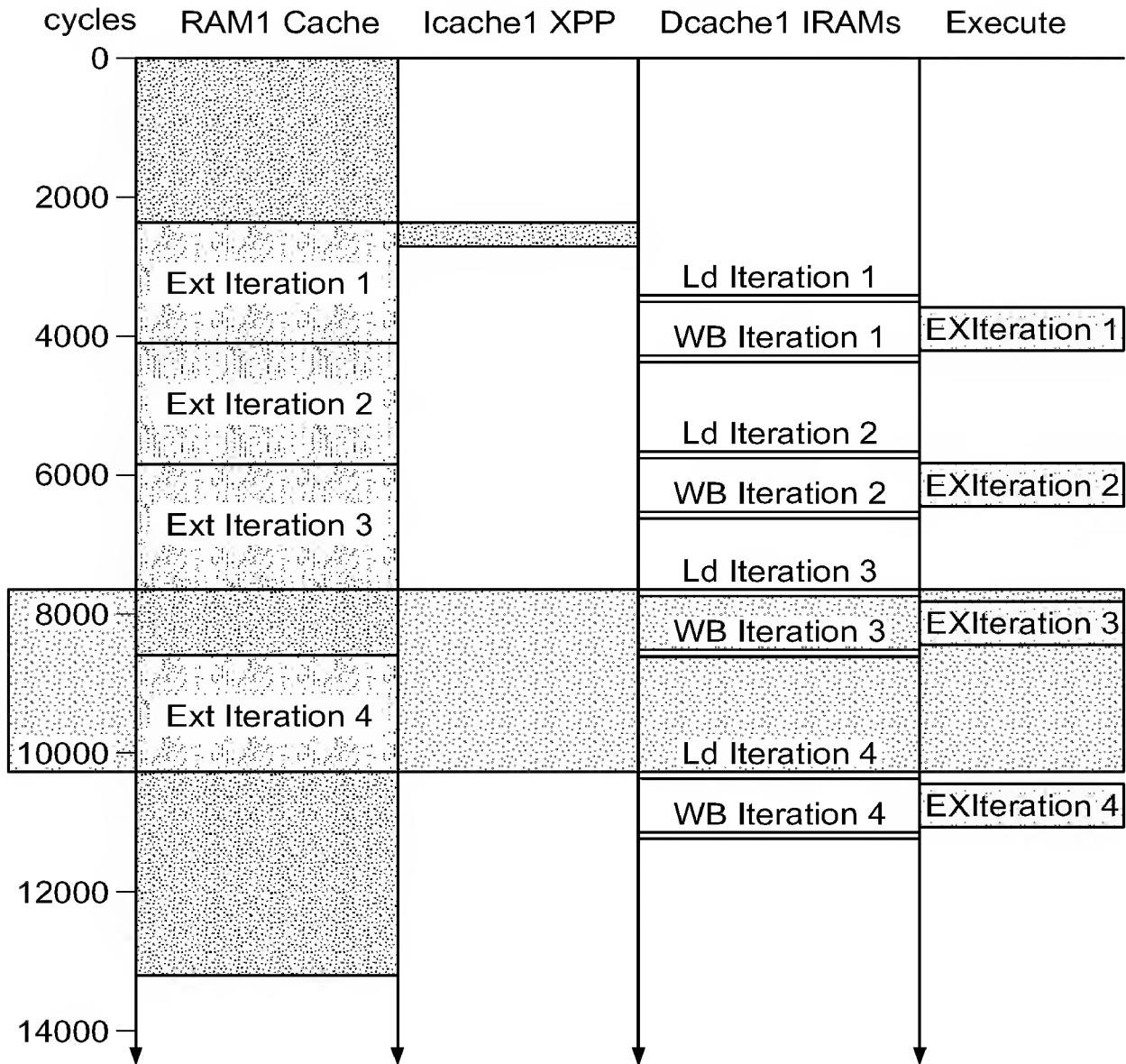


FIG. 20

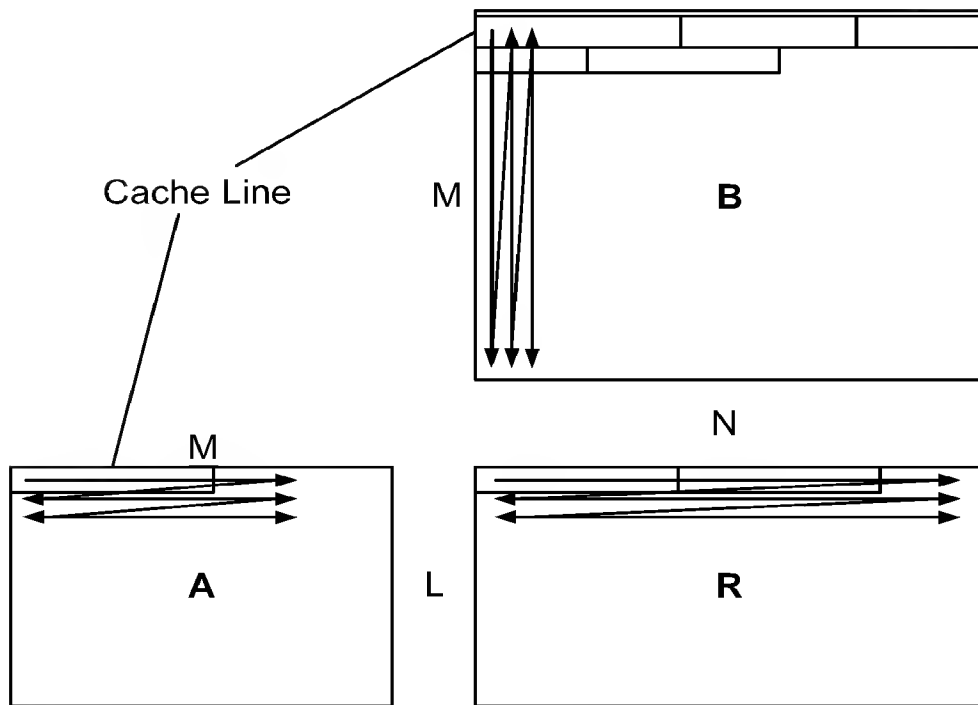


FIG. 21

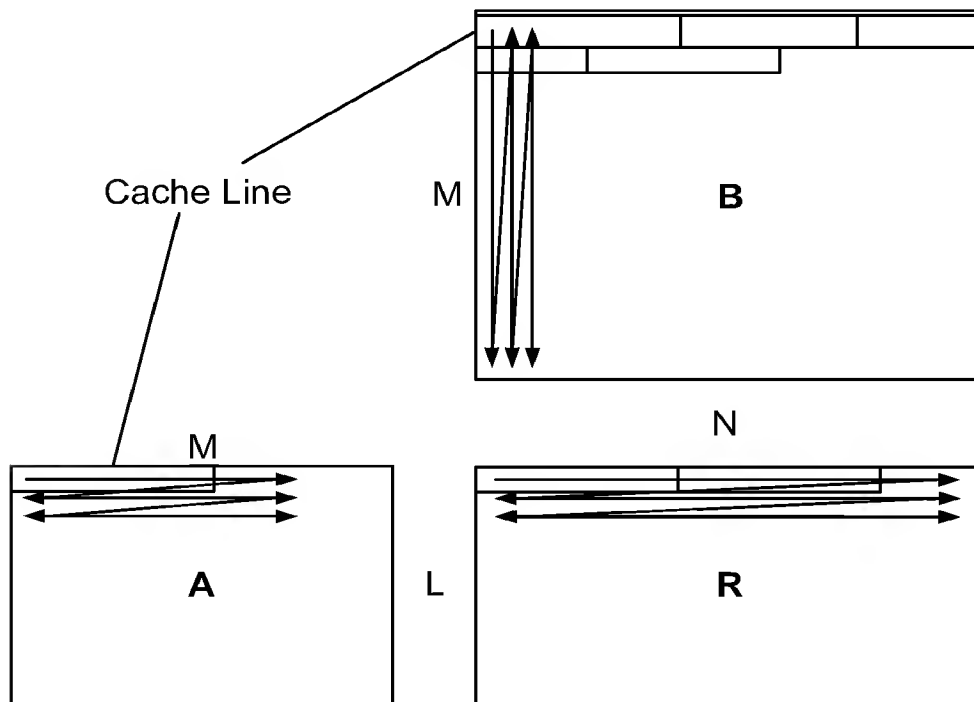


FIG. 22

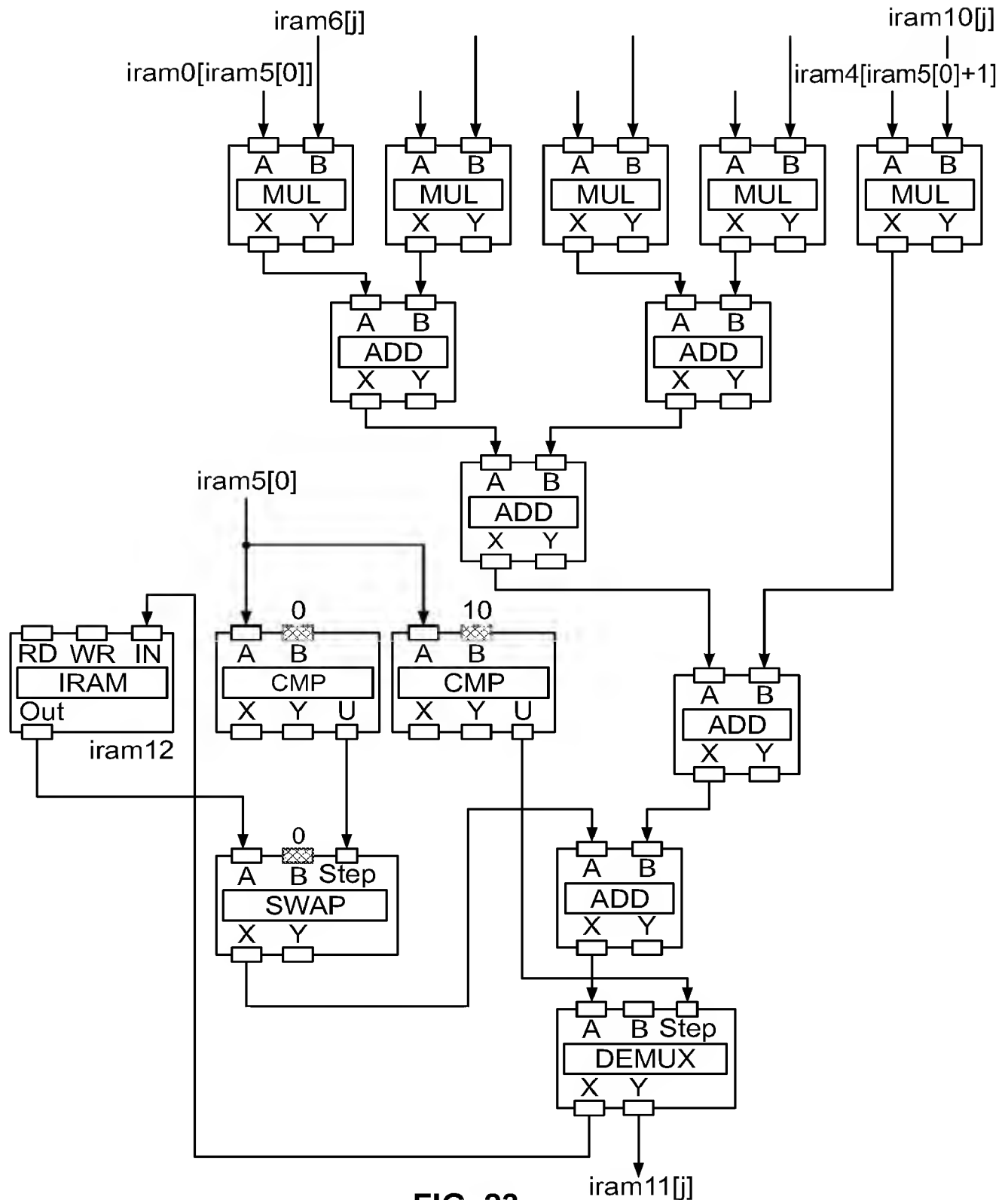


FIG. 23

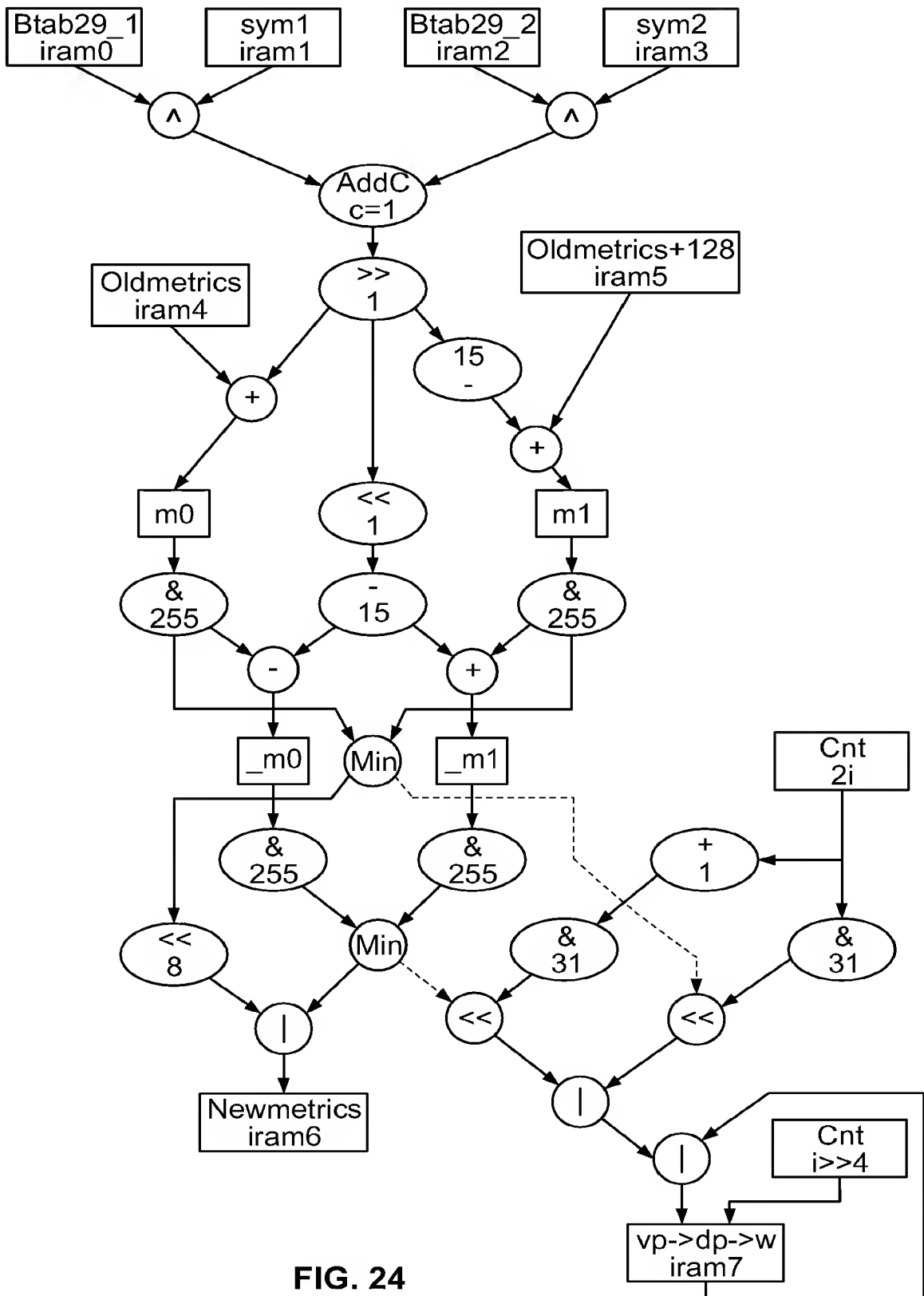


FIG. 24

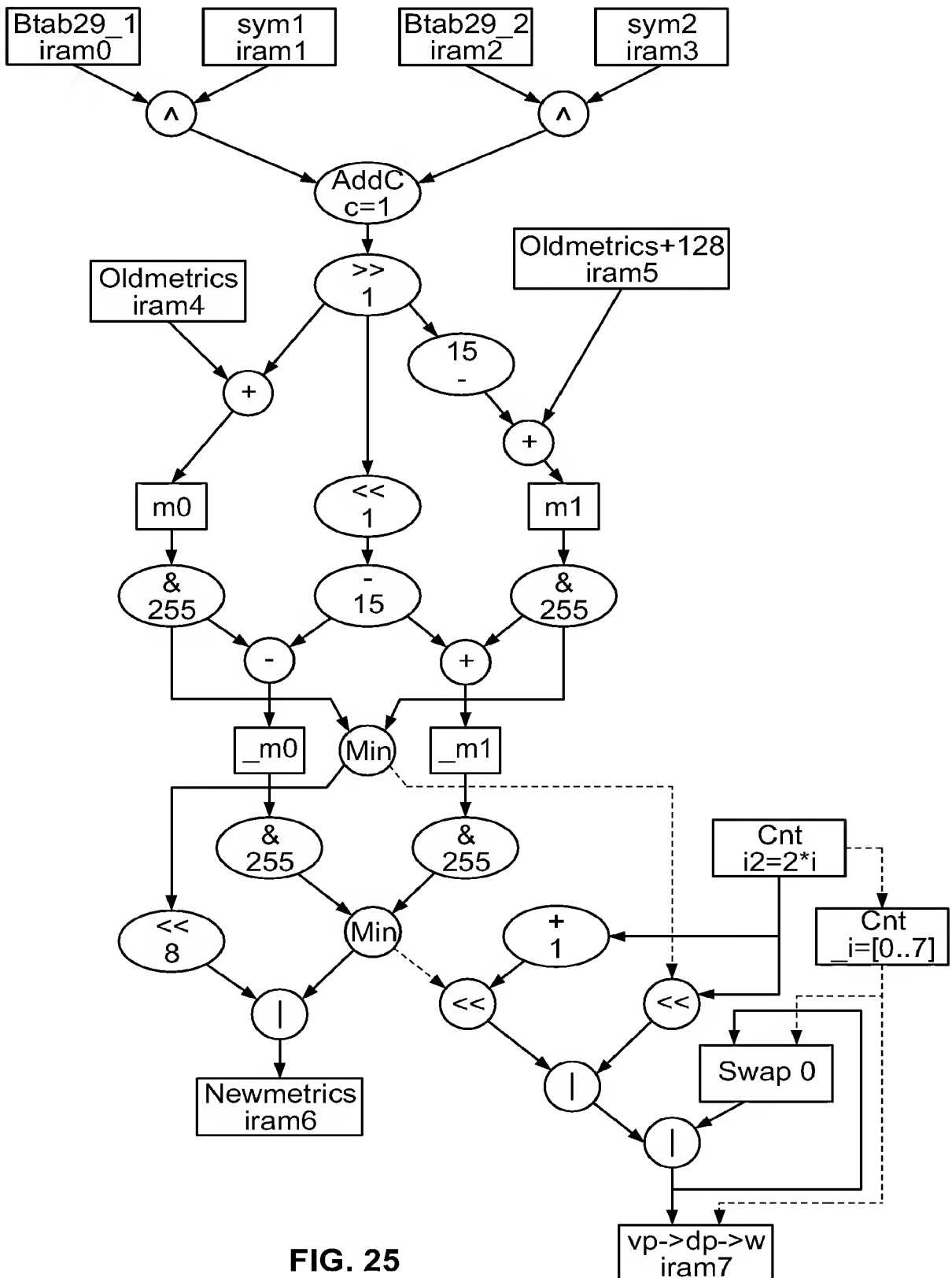


FIG. 25

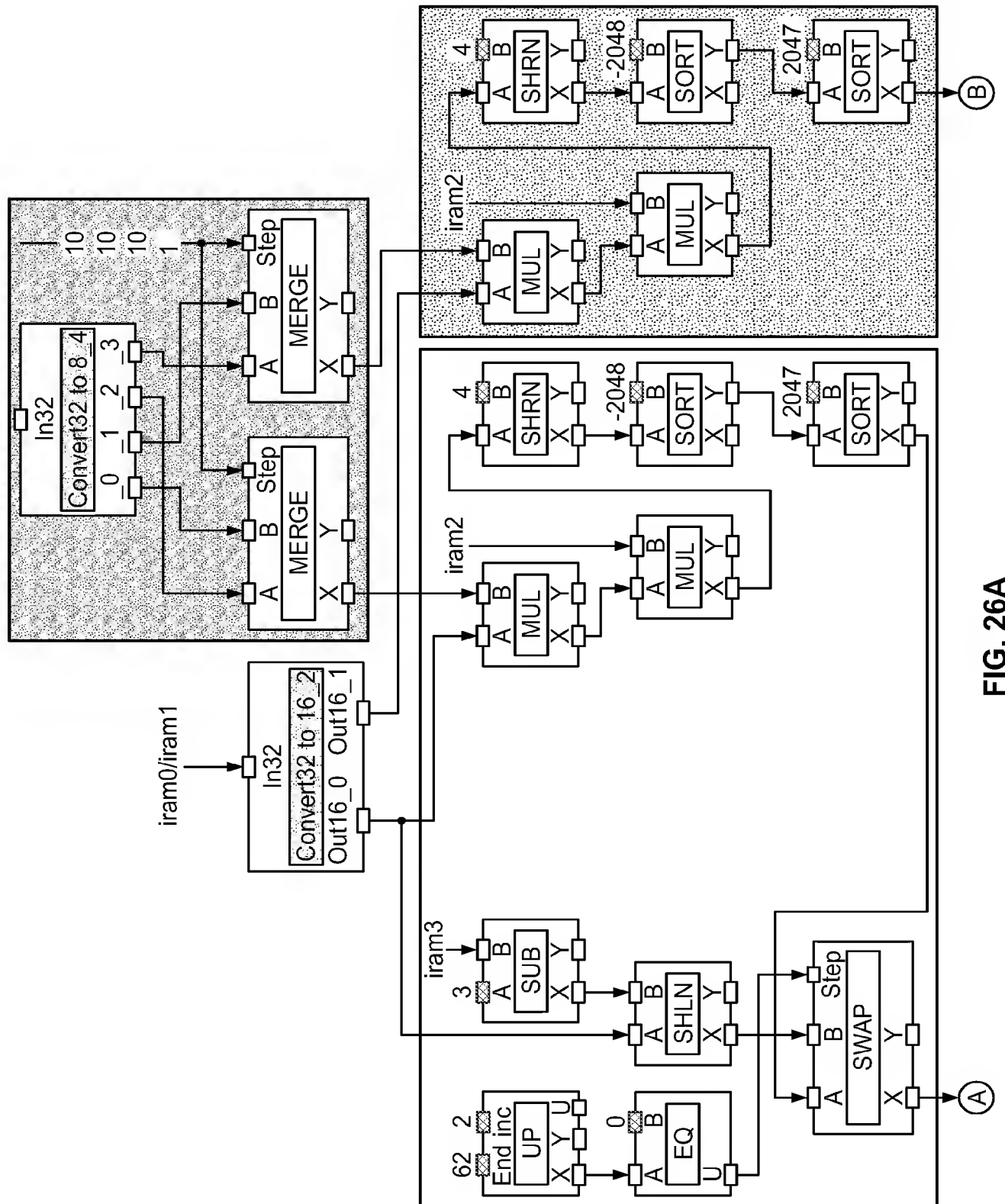


FIG. 26A

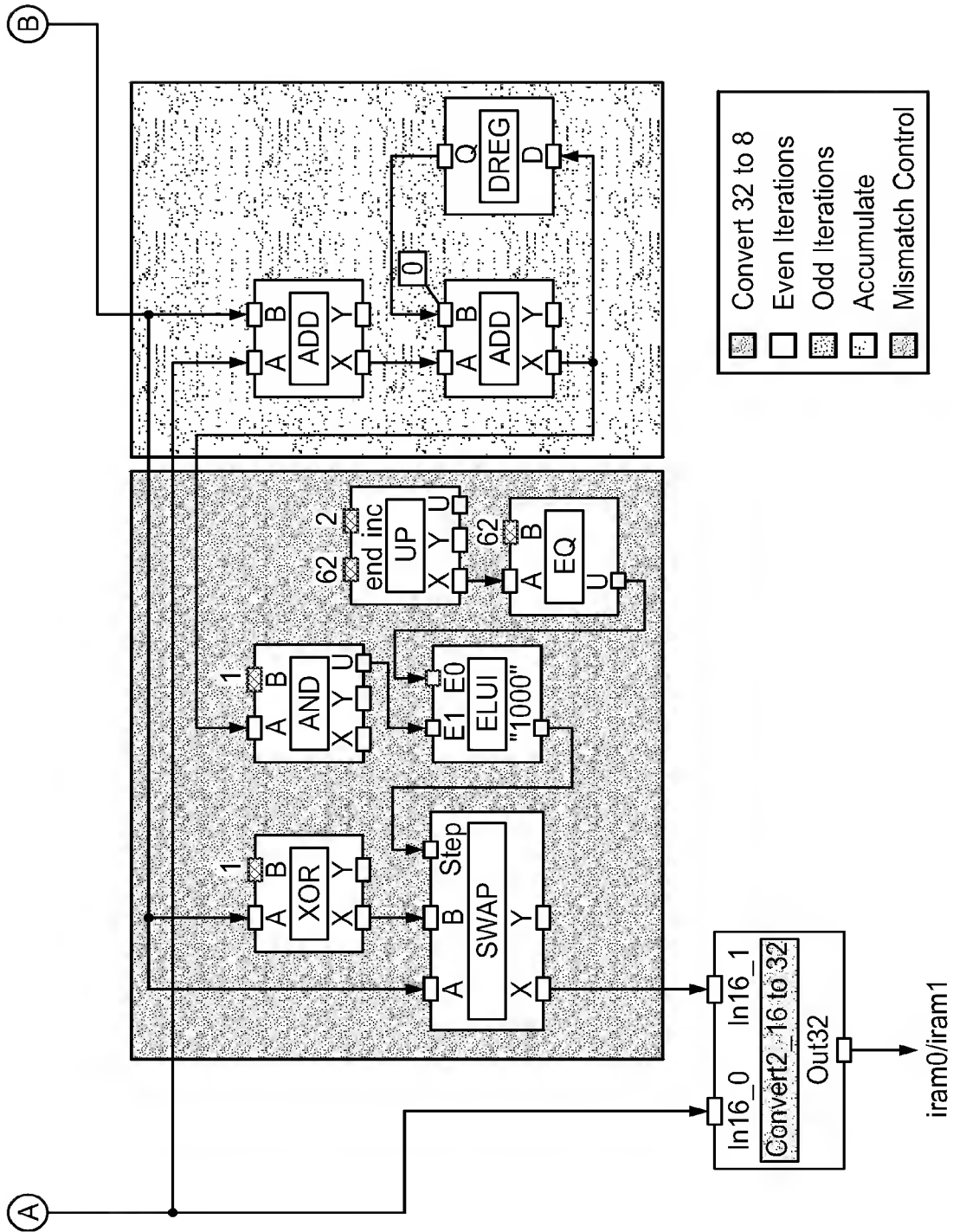


FIG. 26B

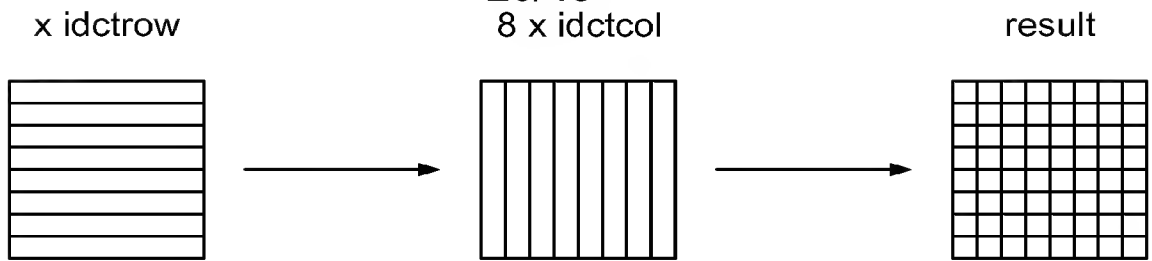


FIG. 27

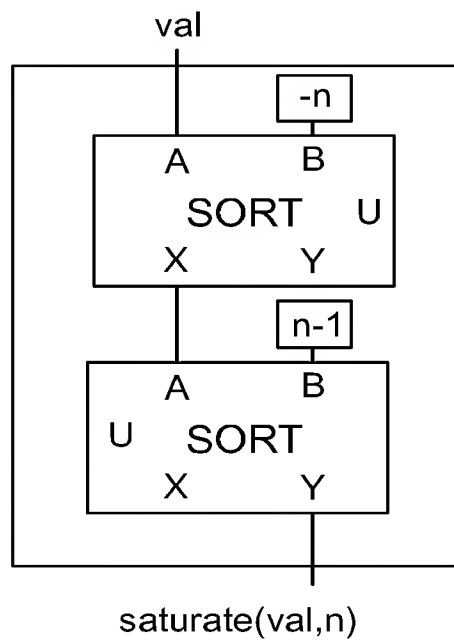


FIG. 28

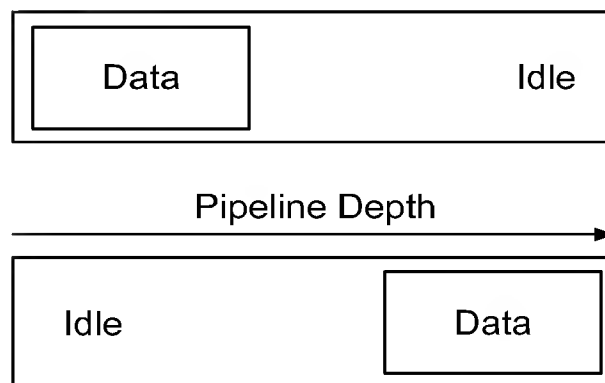


FIG. 29

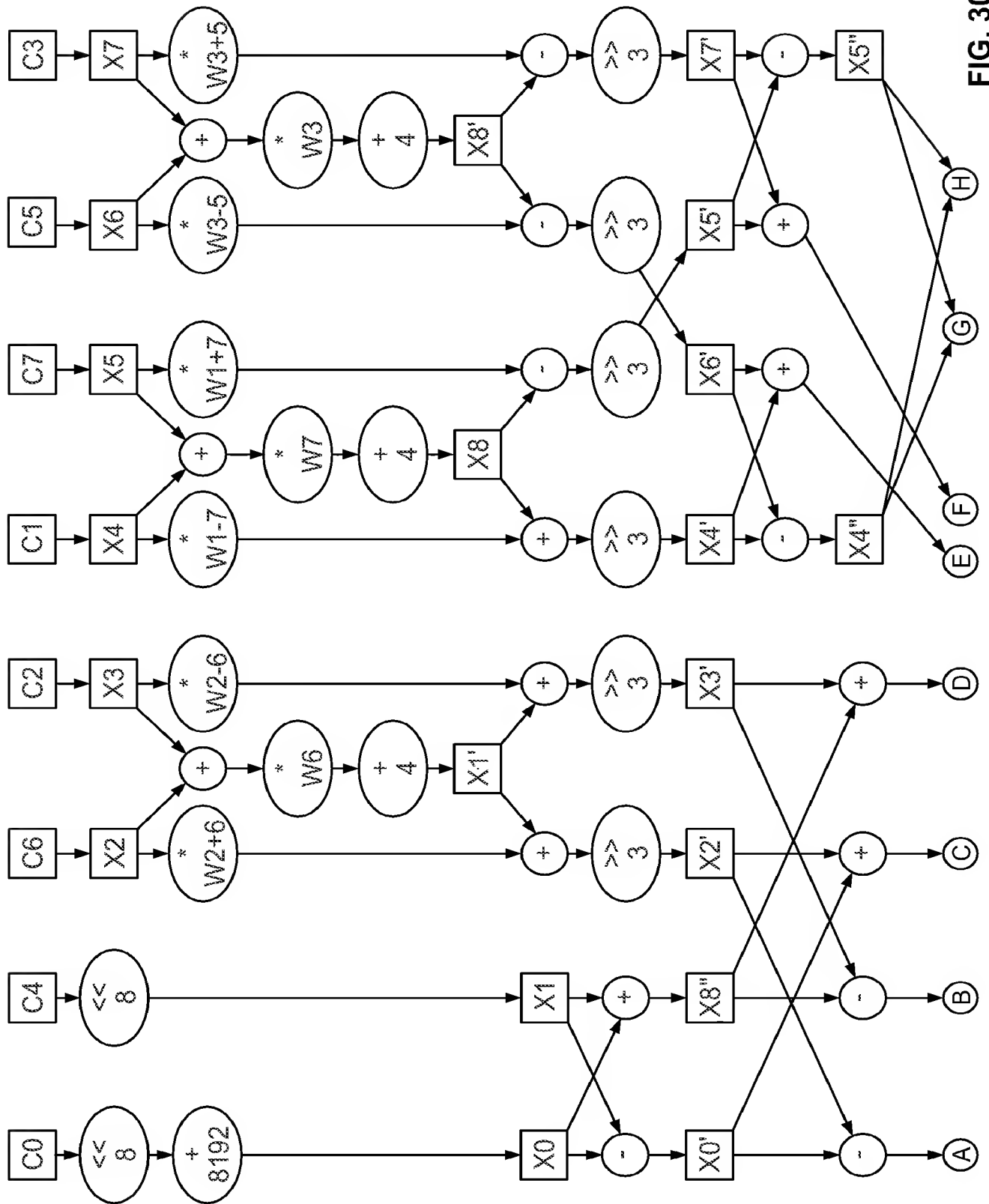


FIG. 30A

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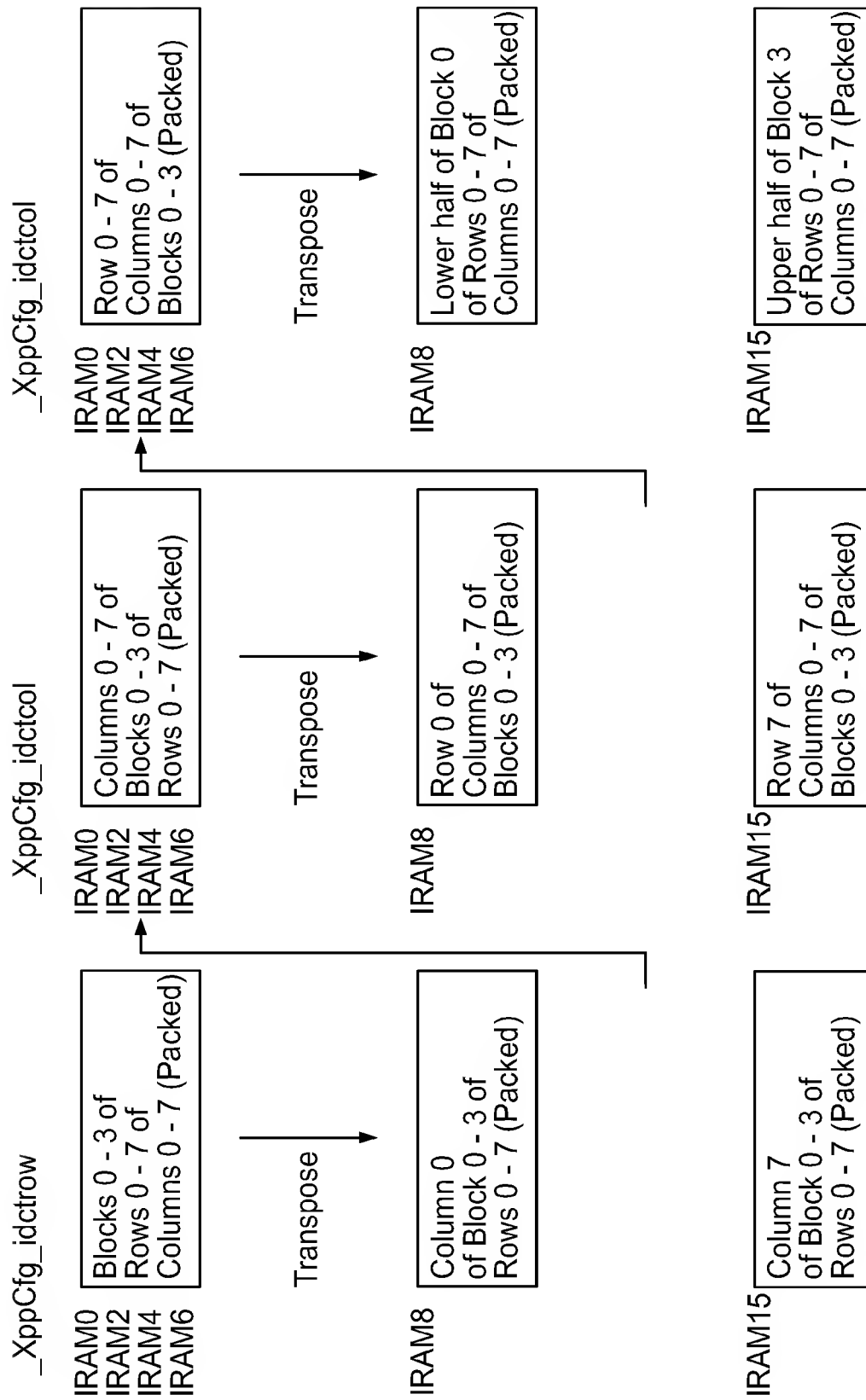


FIG. 31

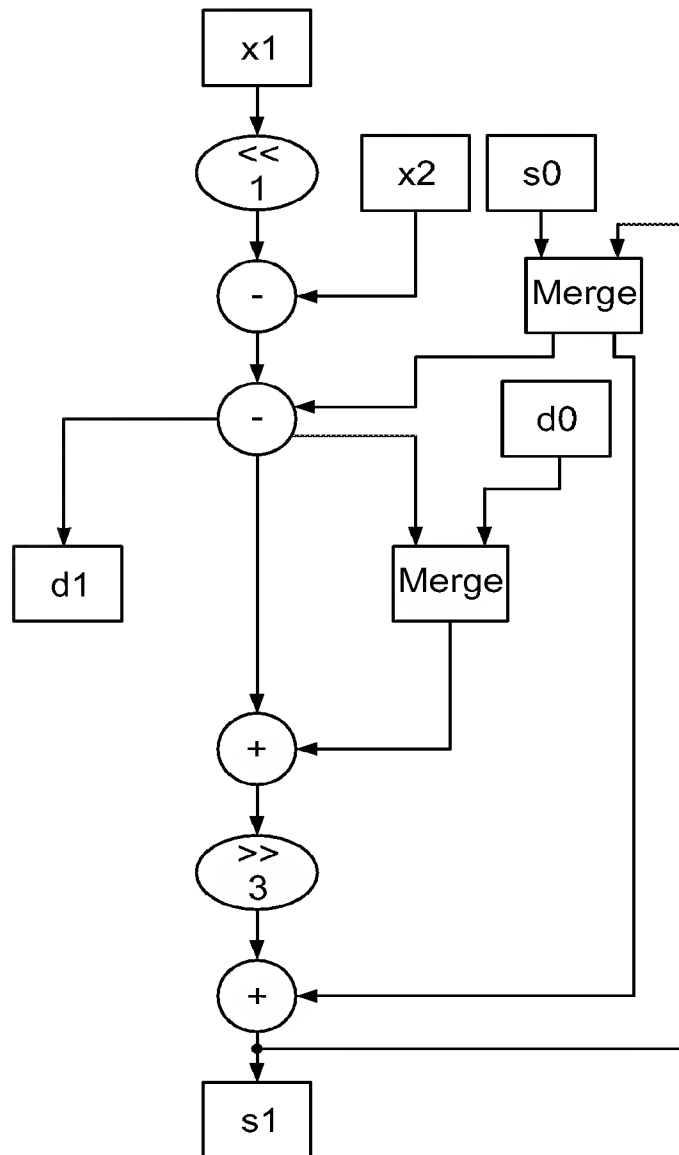


FIG. 32

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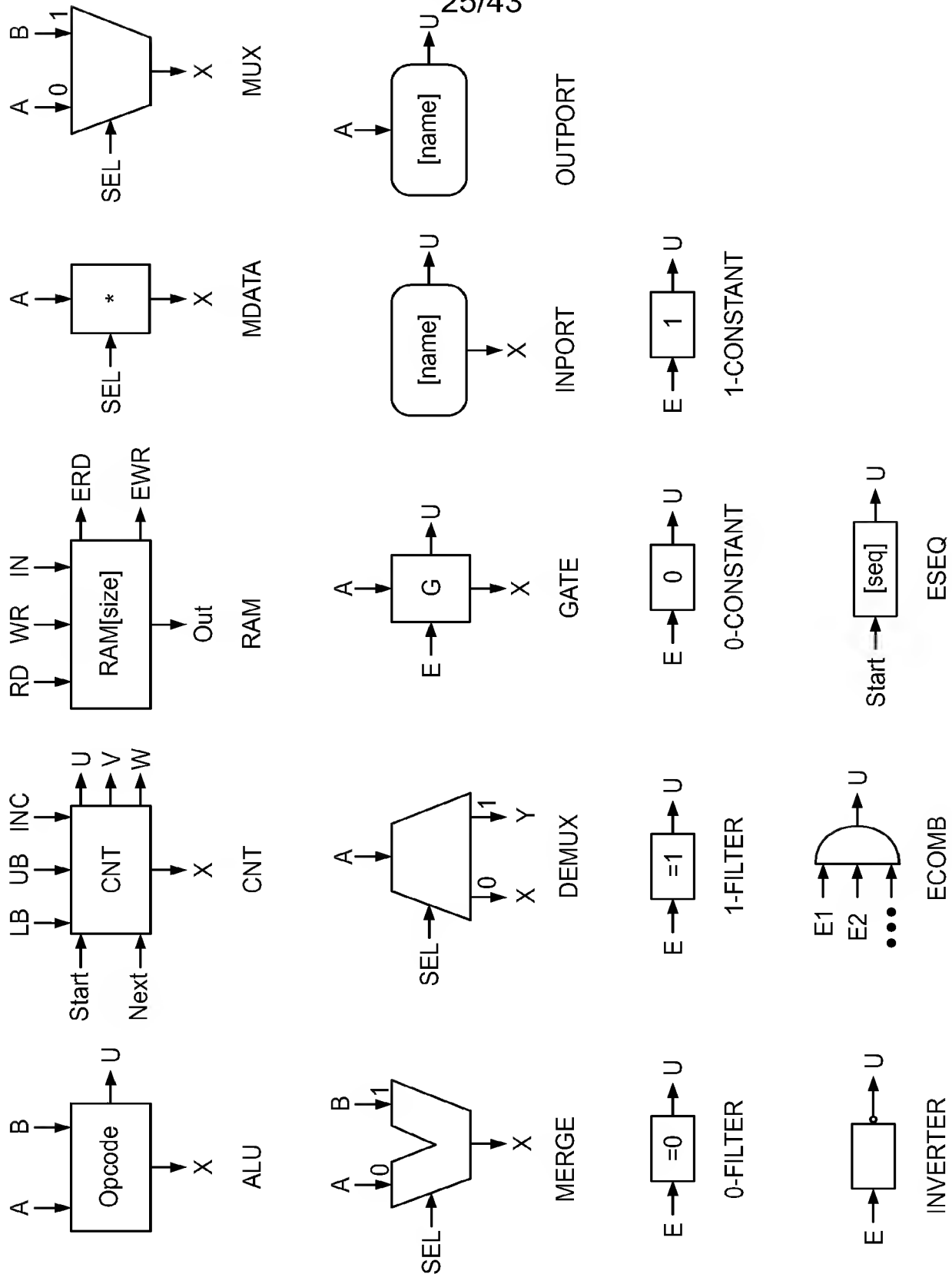


FIG. 33

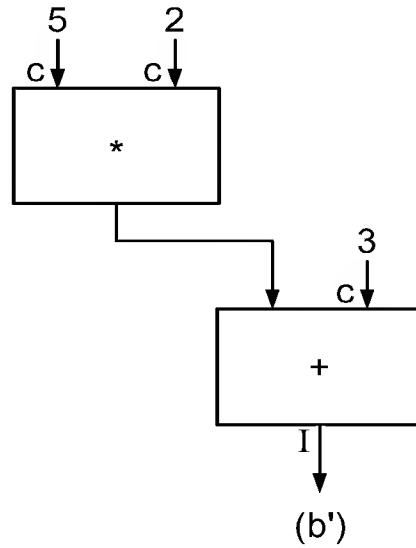


FIG. 34

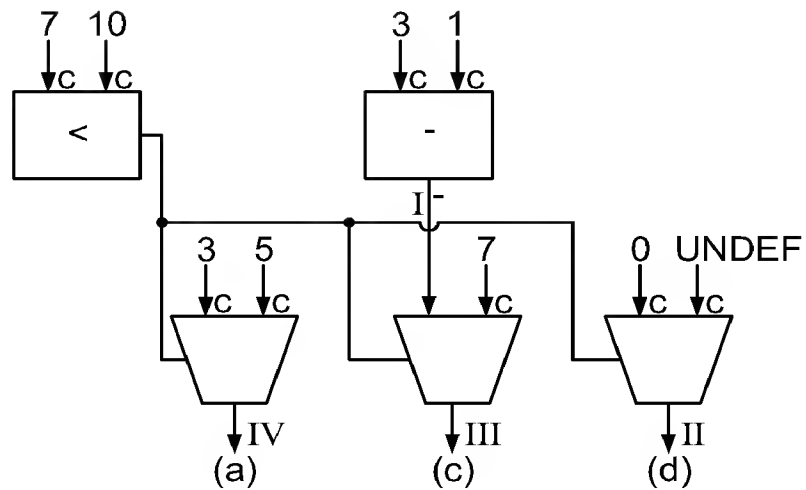


FIG. 35

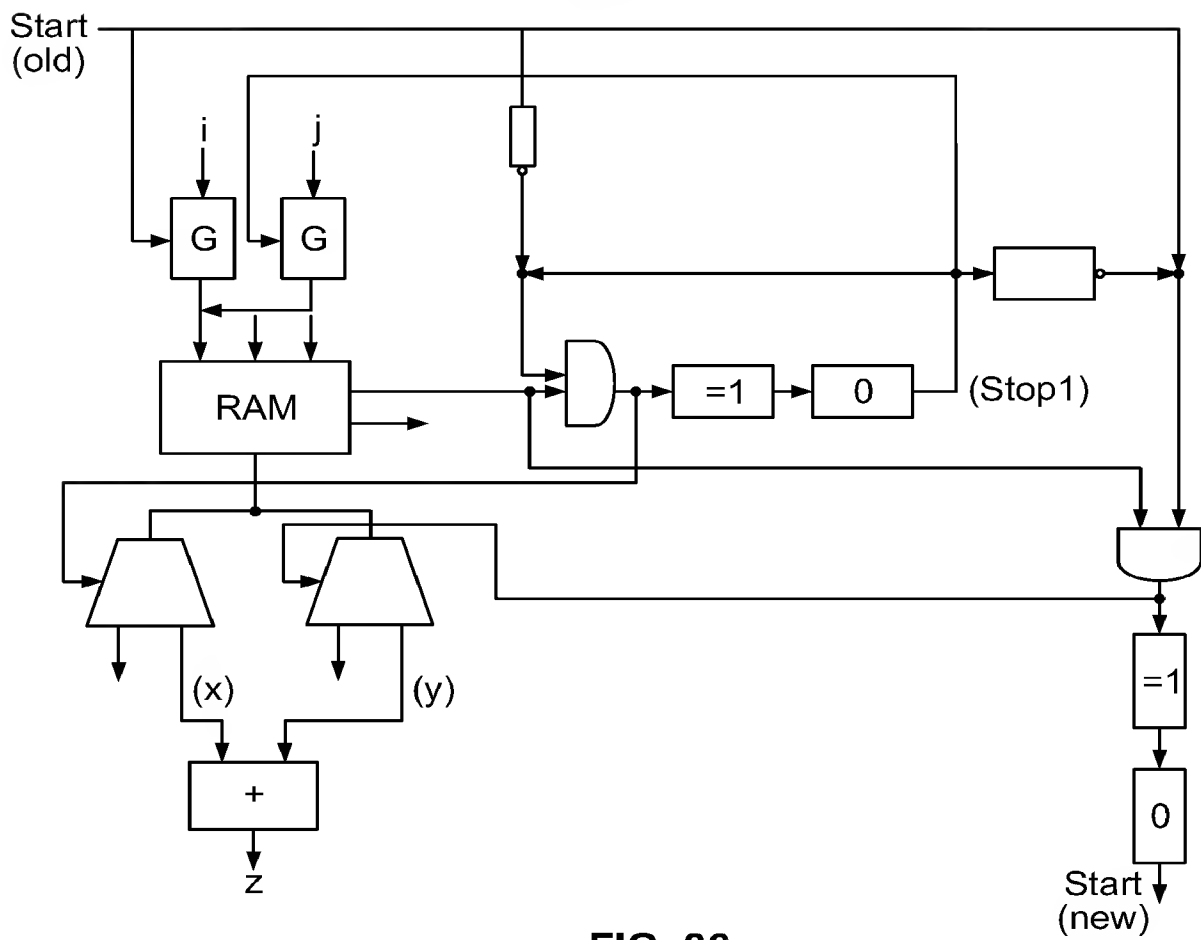


FIG. 36

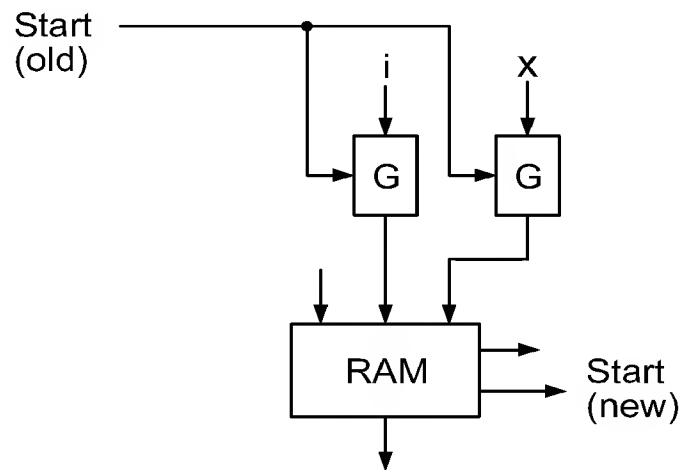


FIG. 37

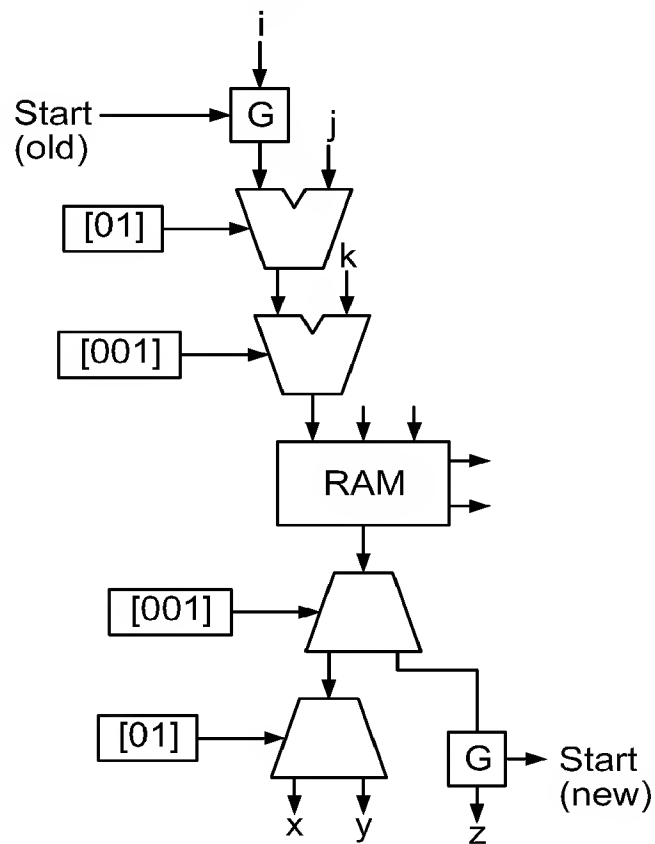


FIG. 38

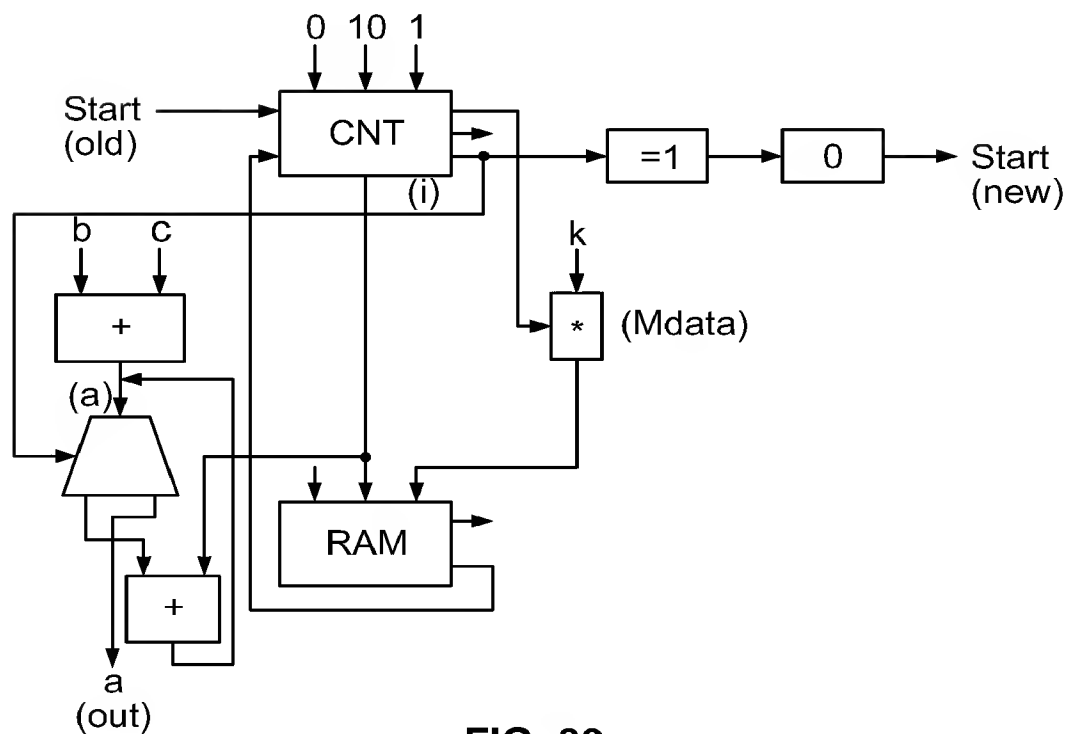


FIG. 39

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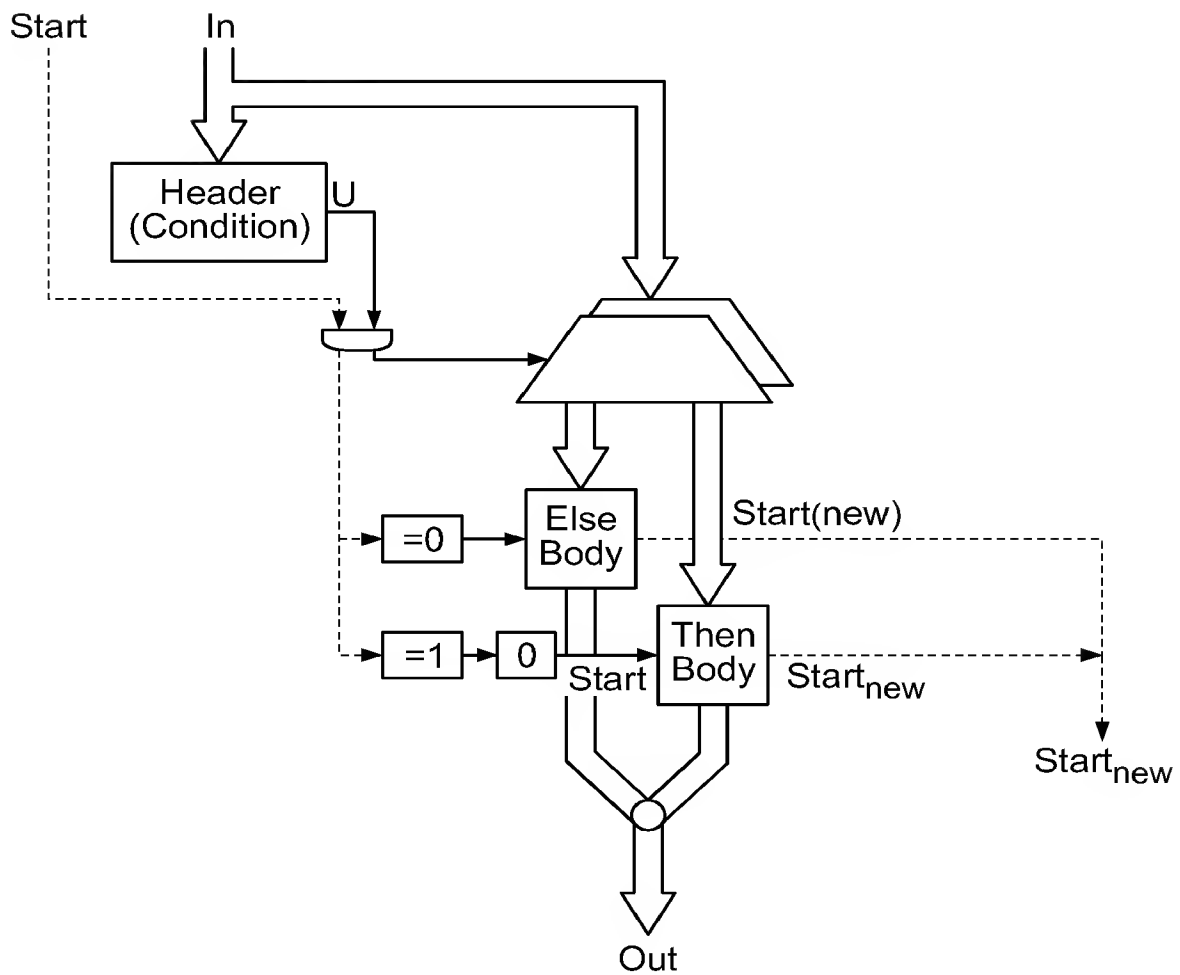


FIG. 40

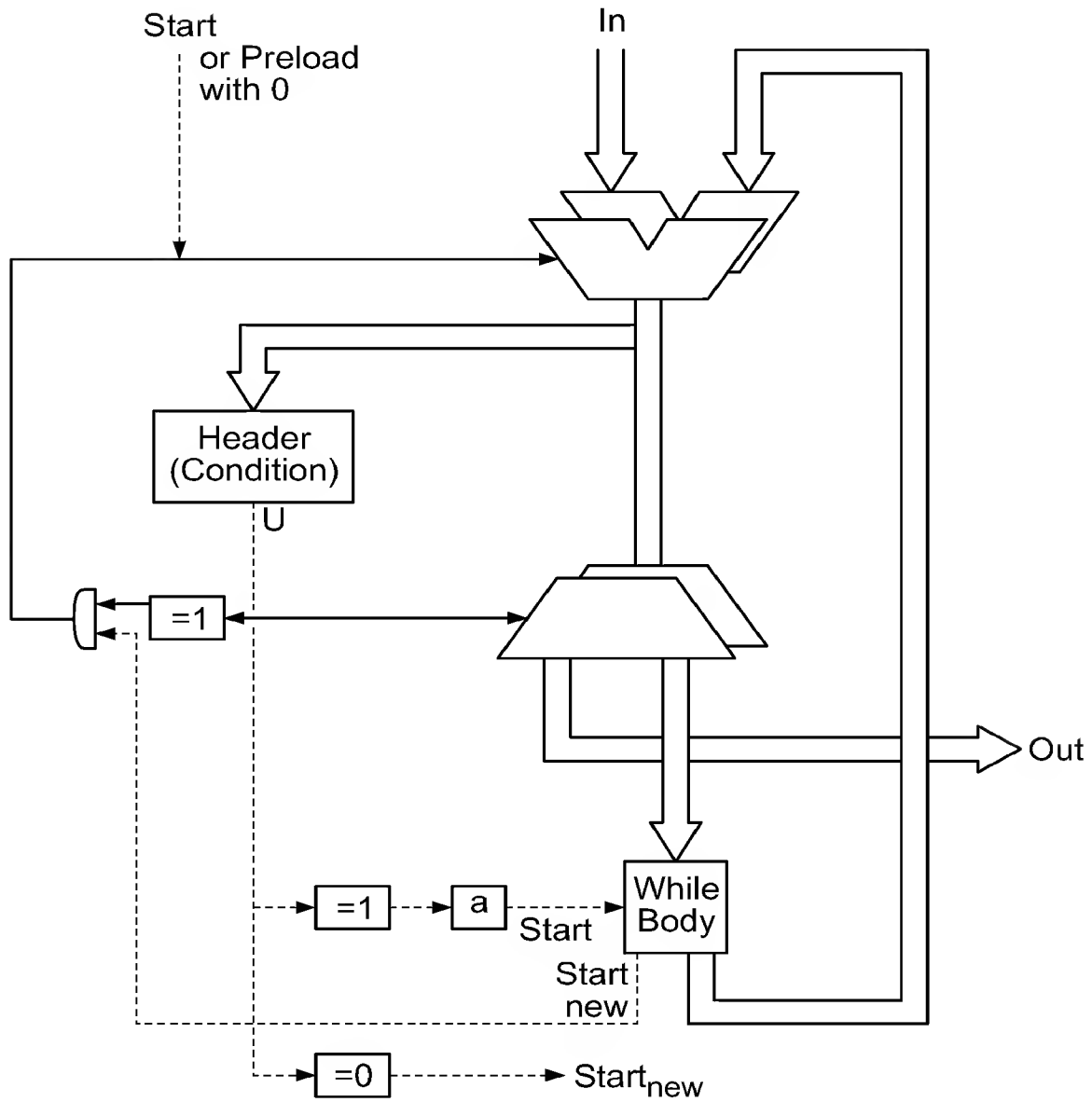


FIG. 41

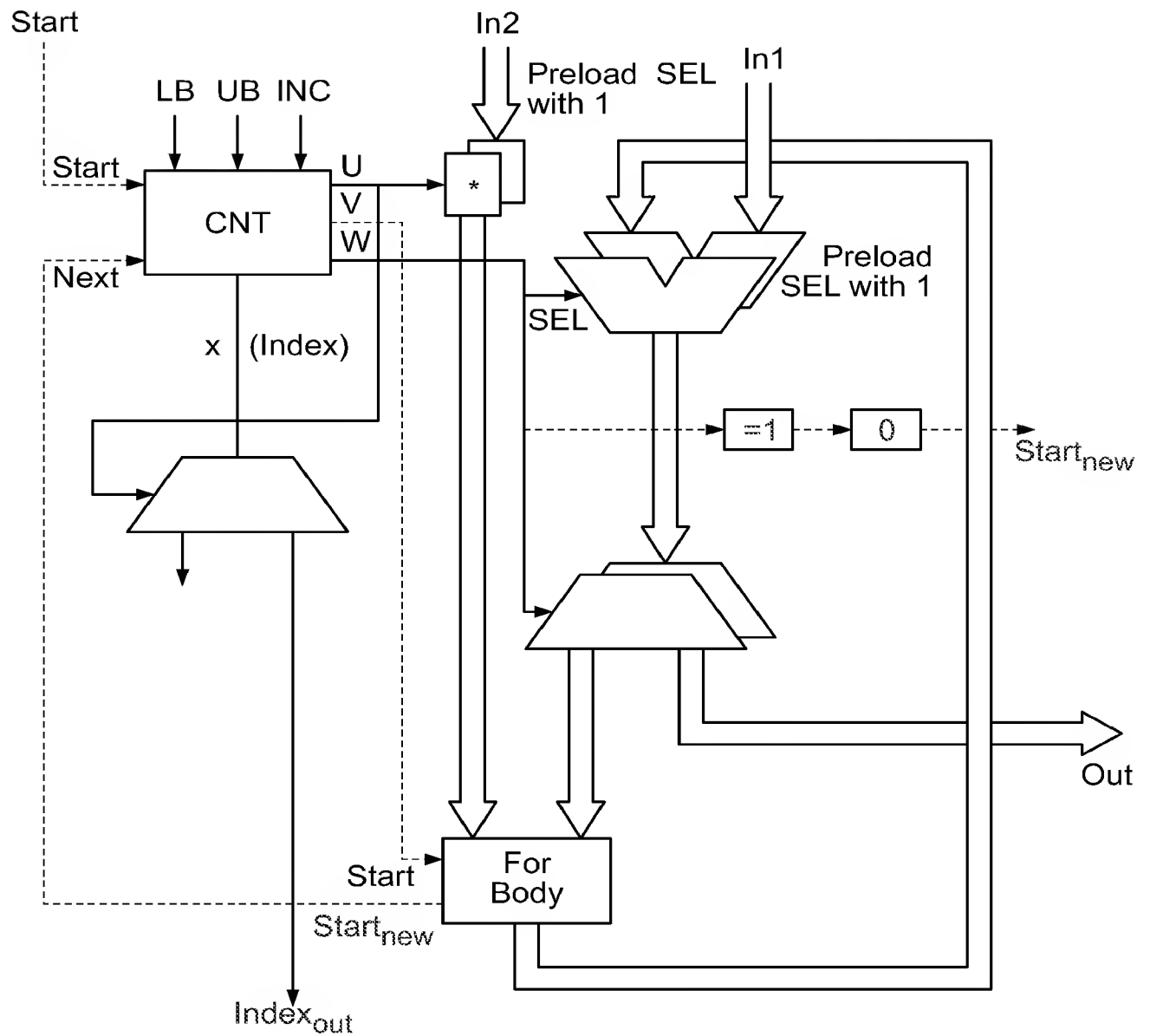


FIG. 42

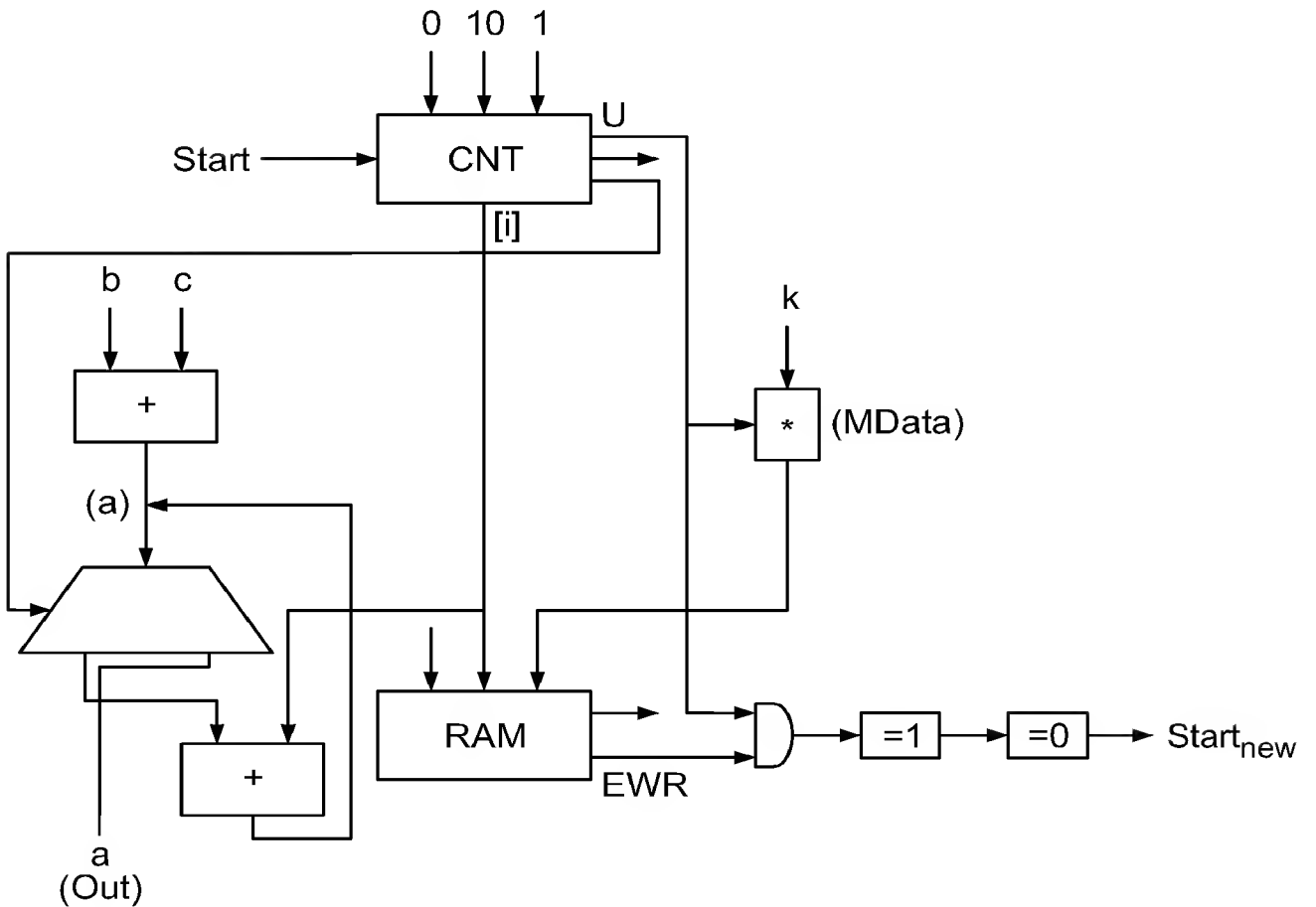


FIG. 43

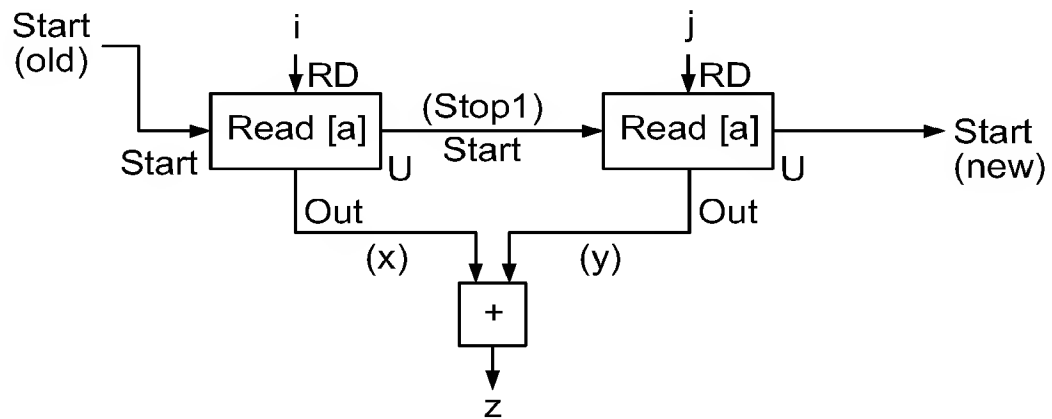


FIG. 44

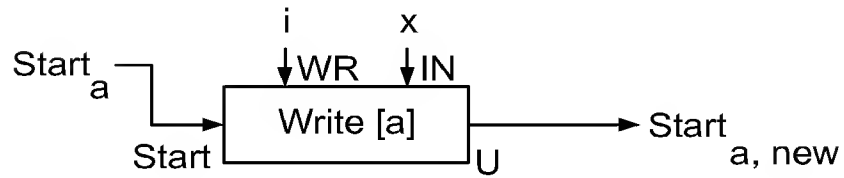


FIG. 45

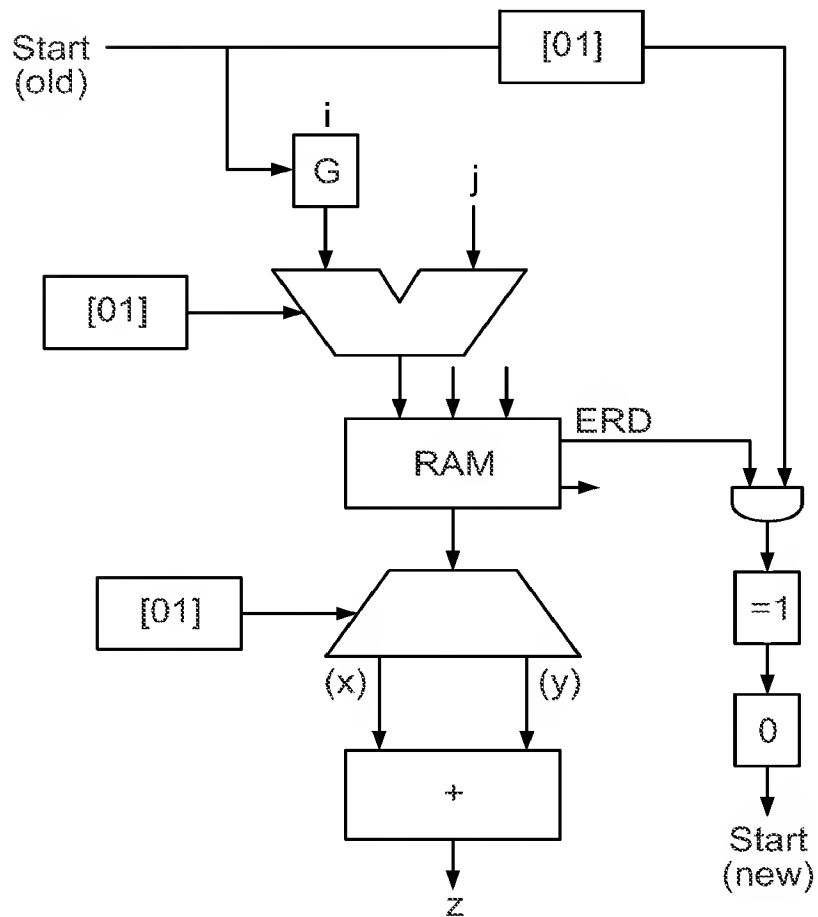


FIG. 46

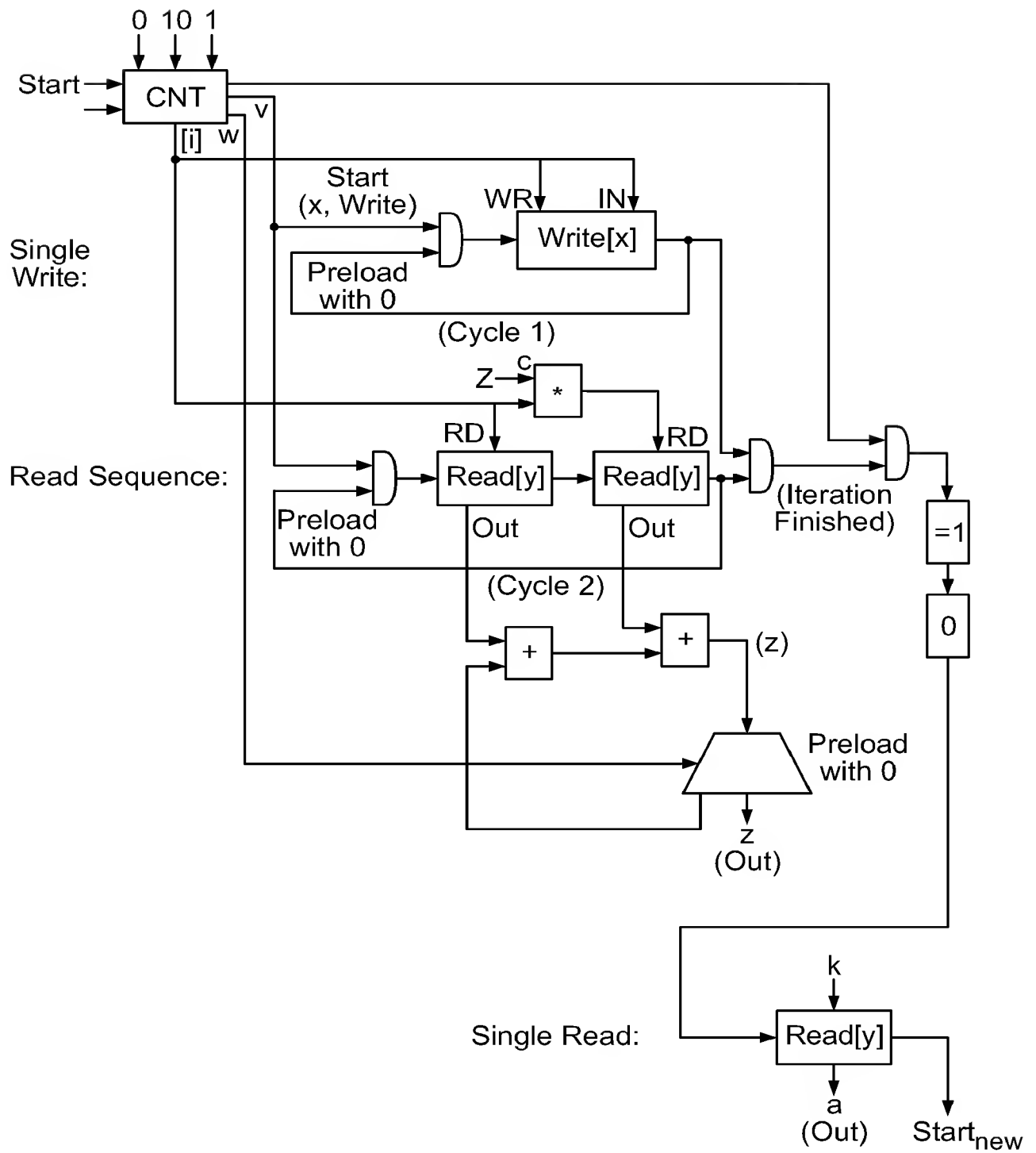


FIG. 47

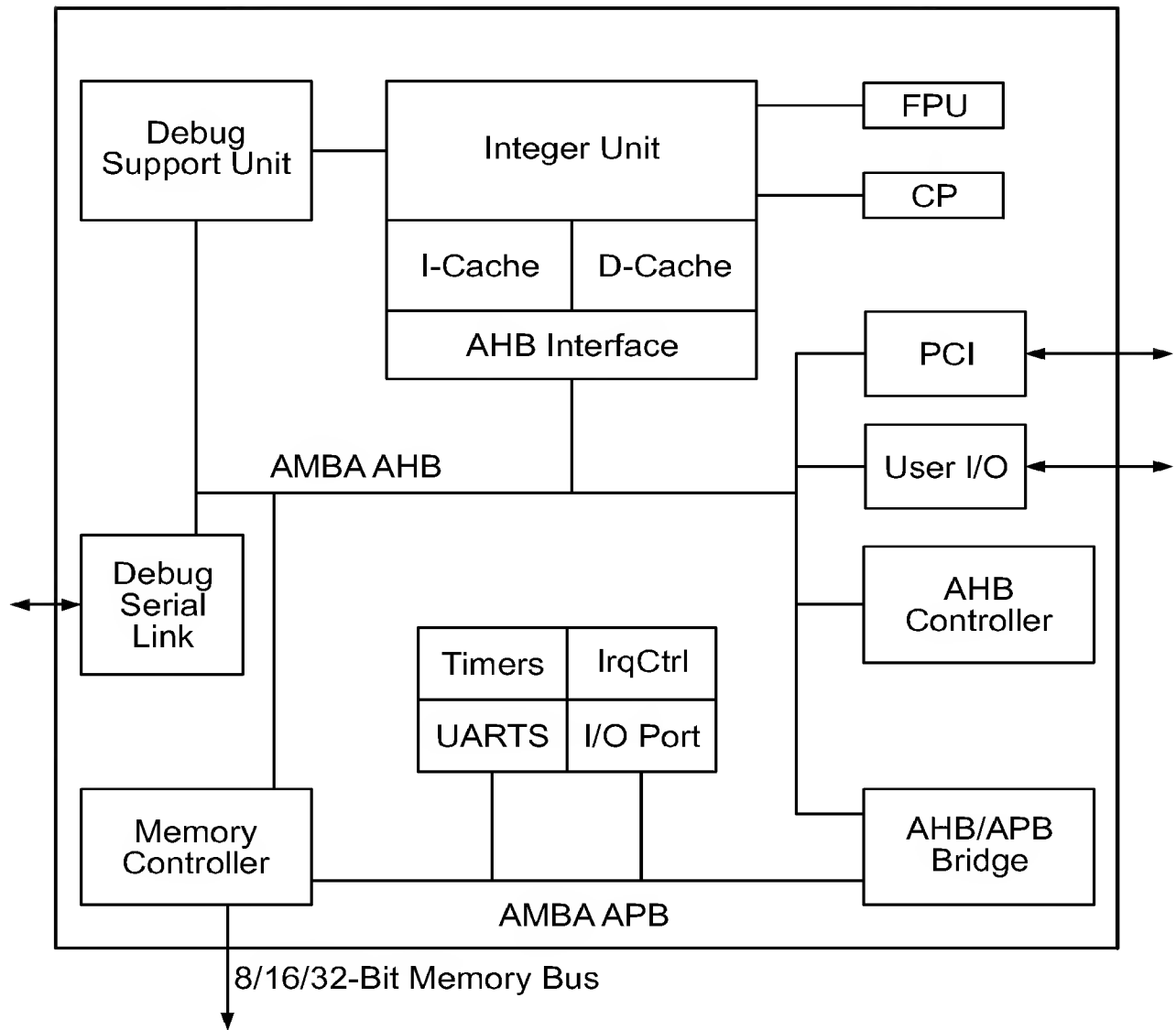


FIG. 49

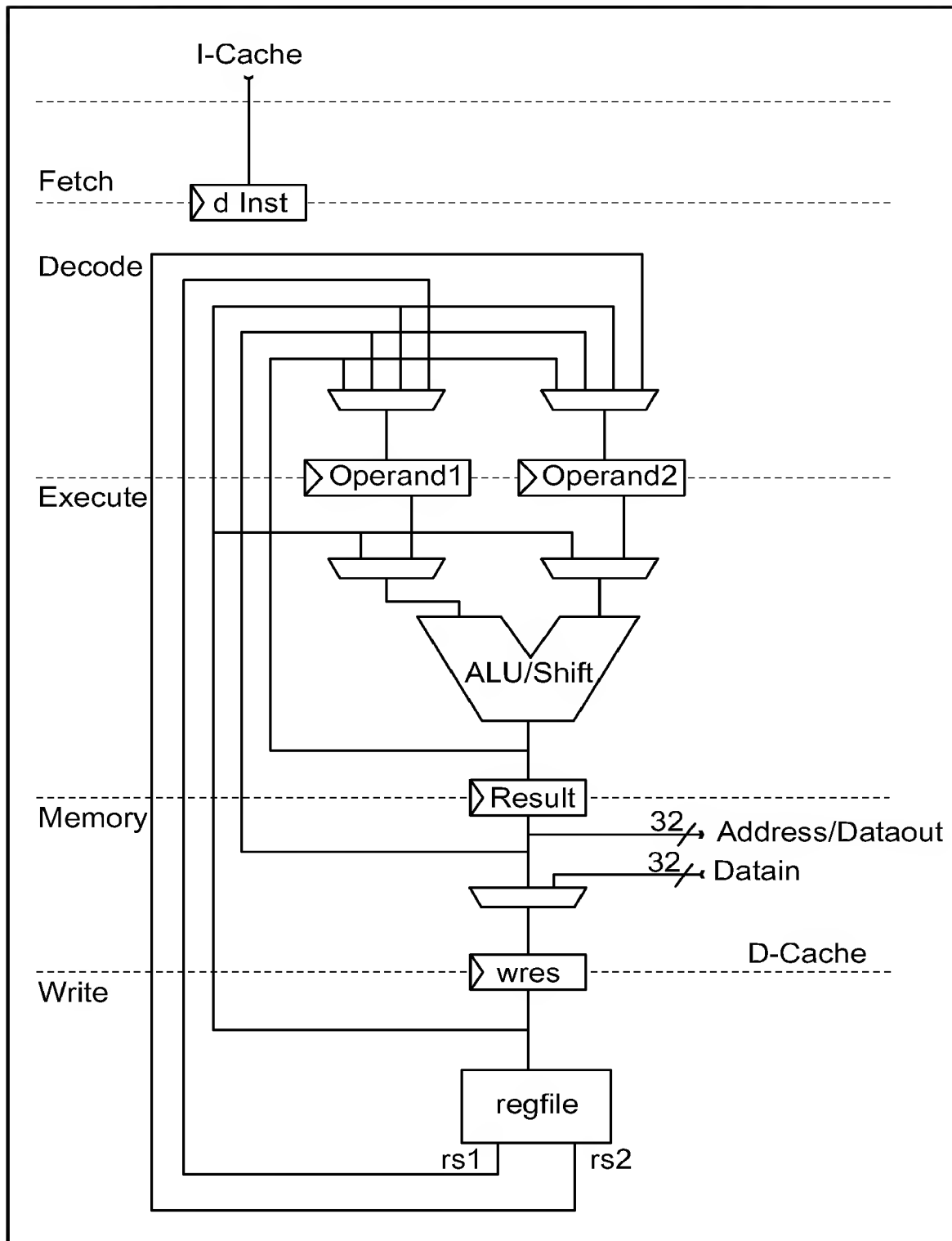


FIG. 50

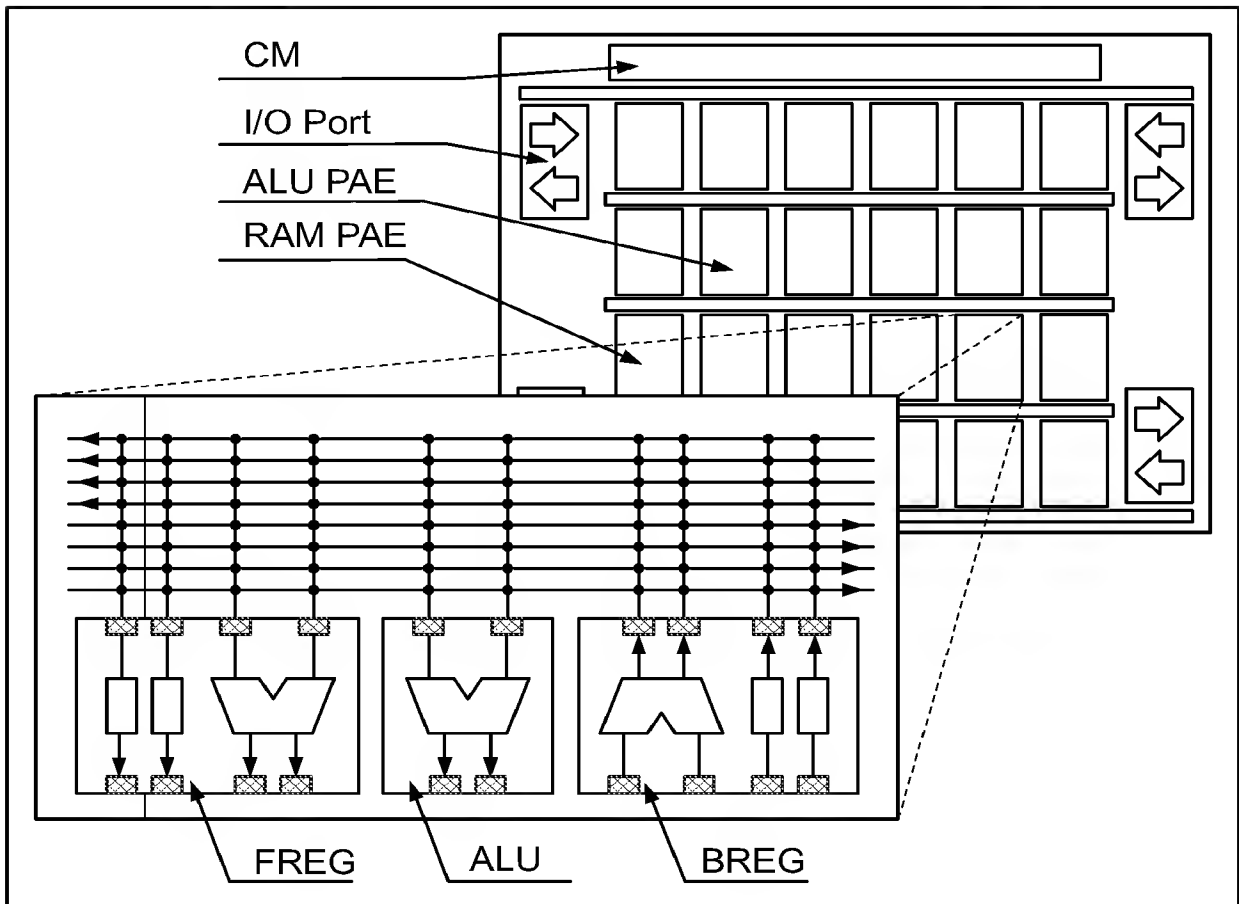


FIG. 51

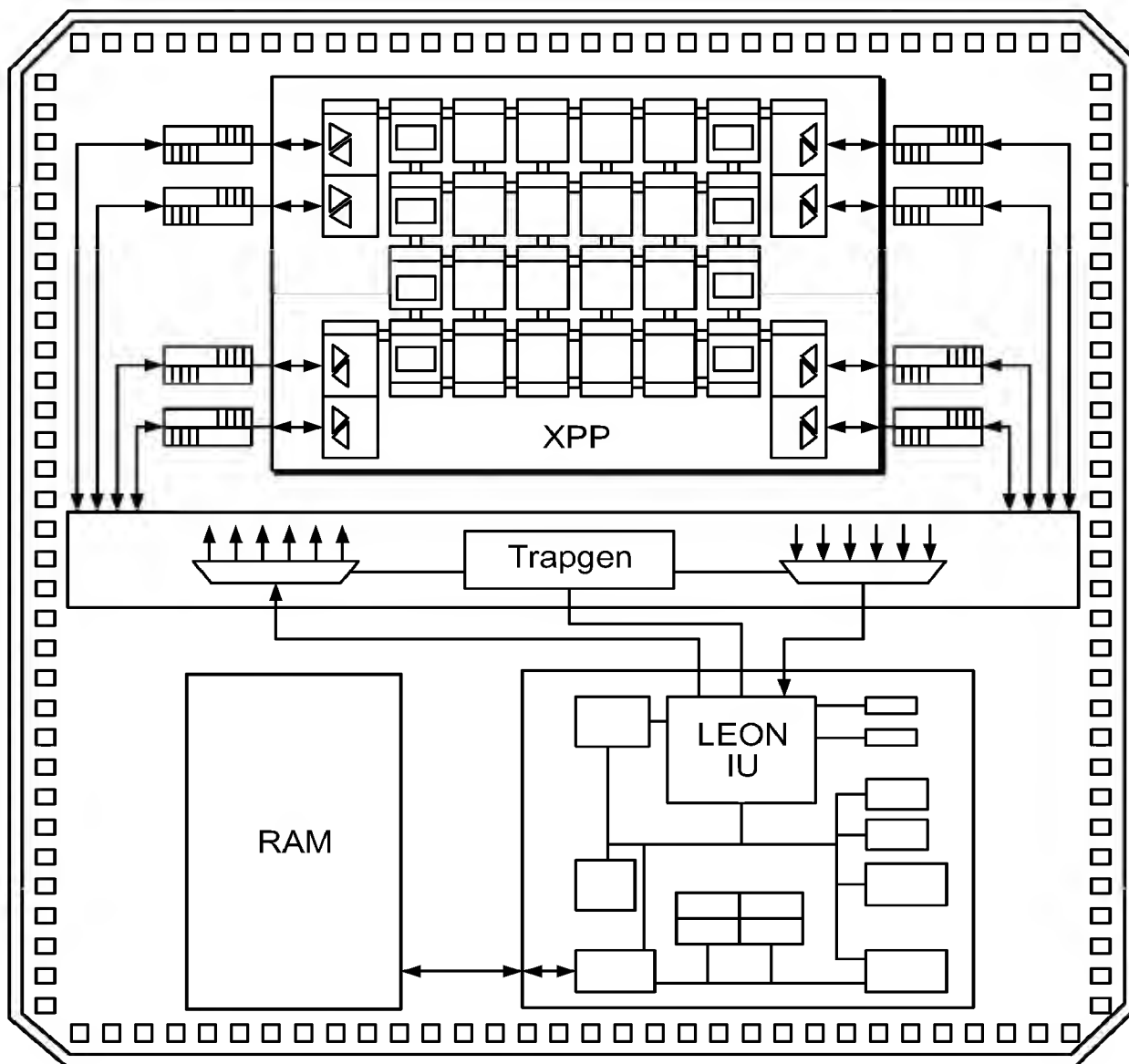


FIG. 52

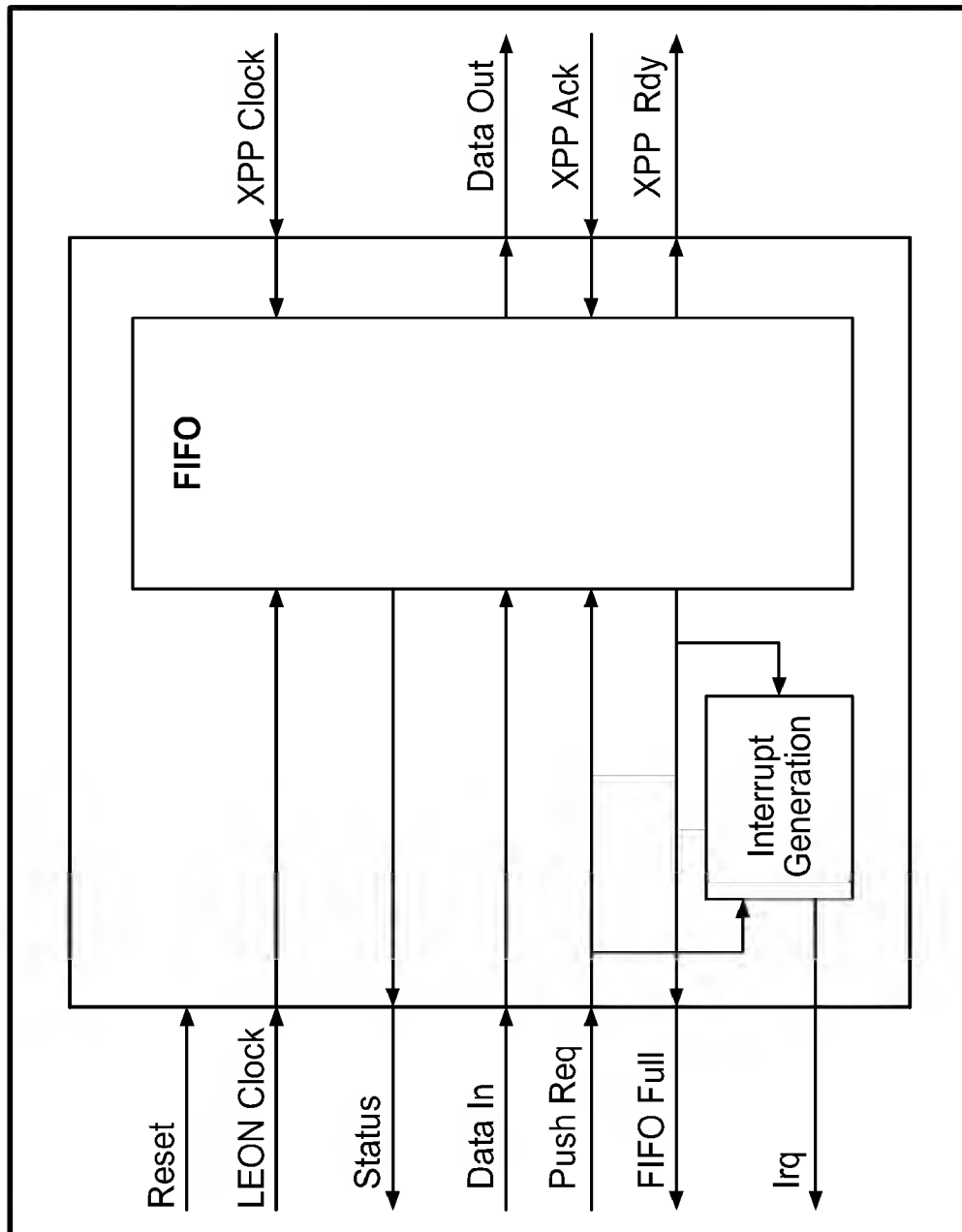


FIG. 53

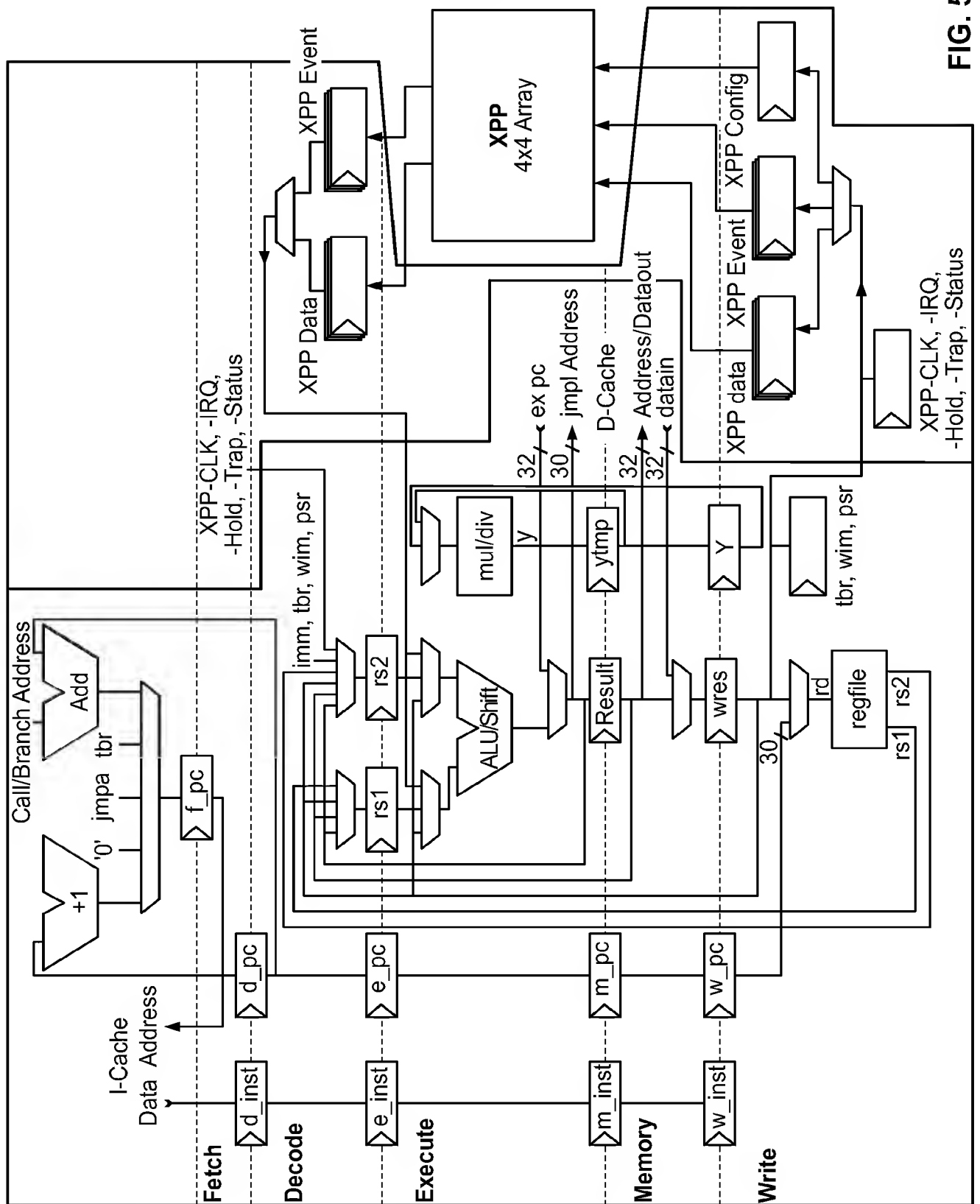


FIG. 54

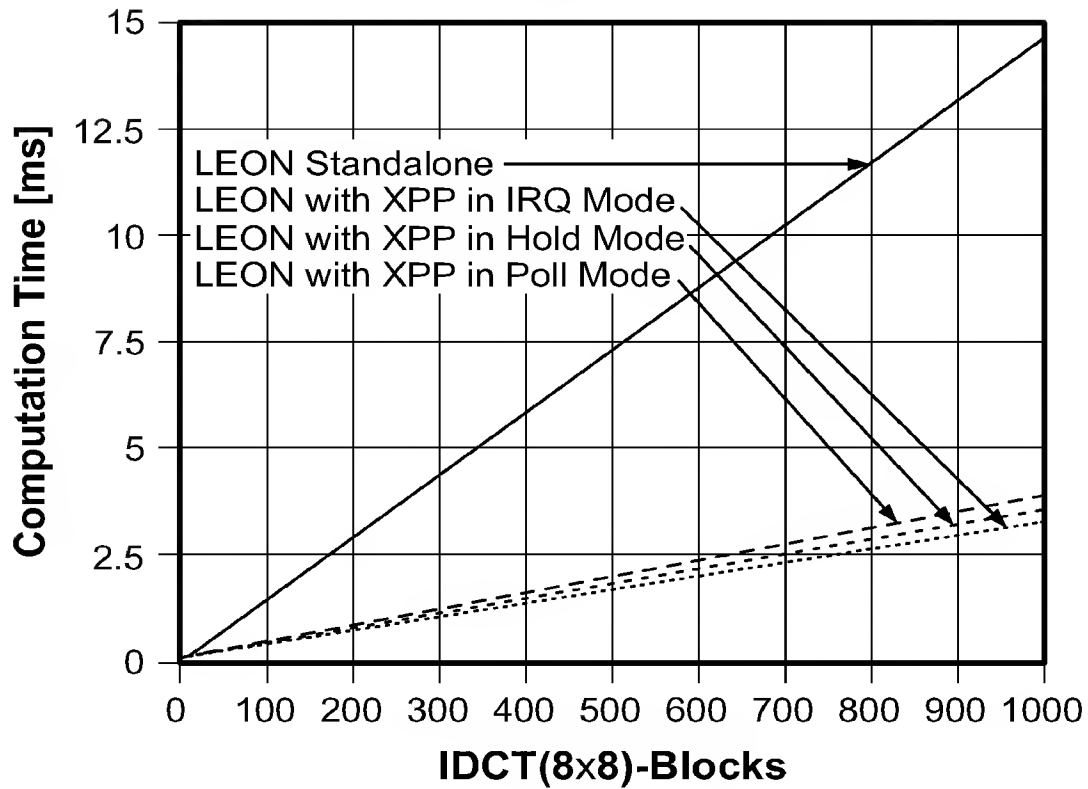


FIG. 55

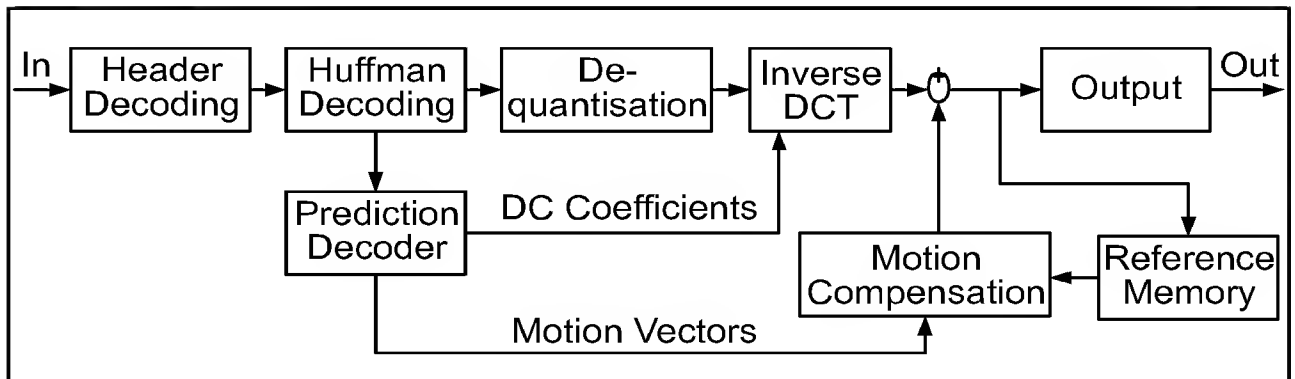


FIG. 56

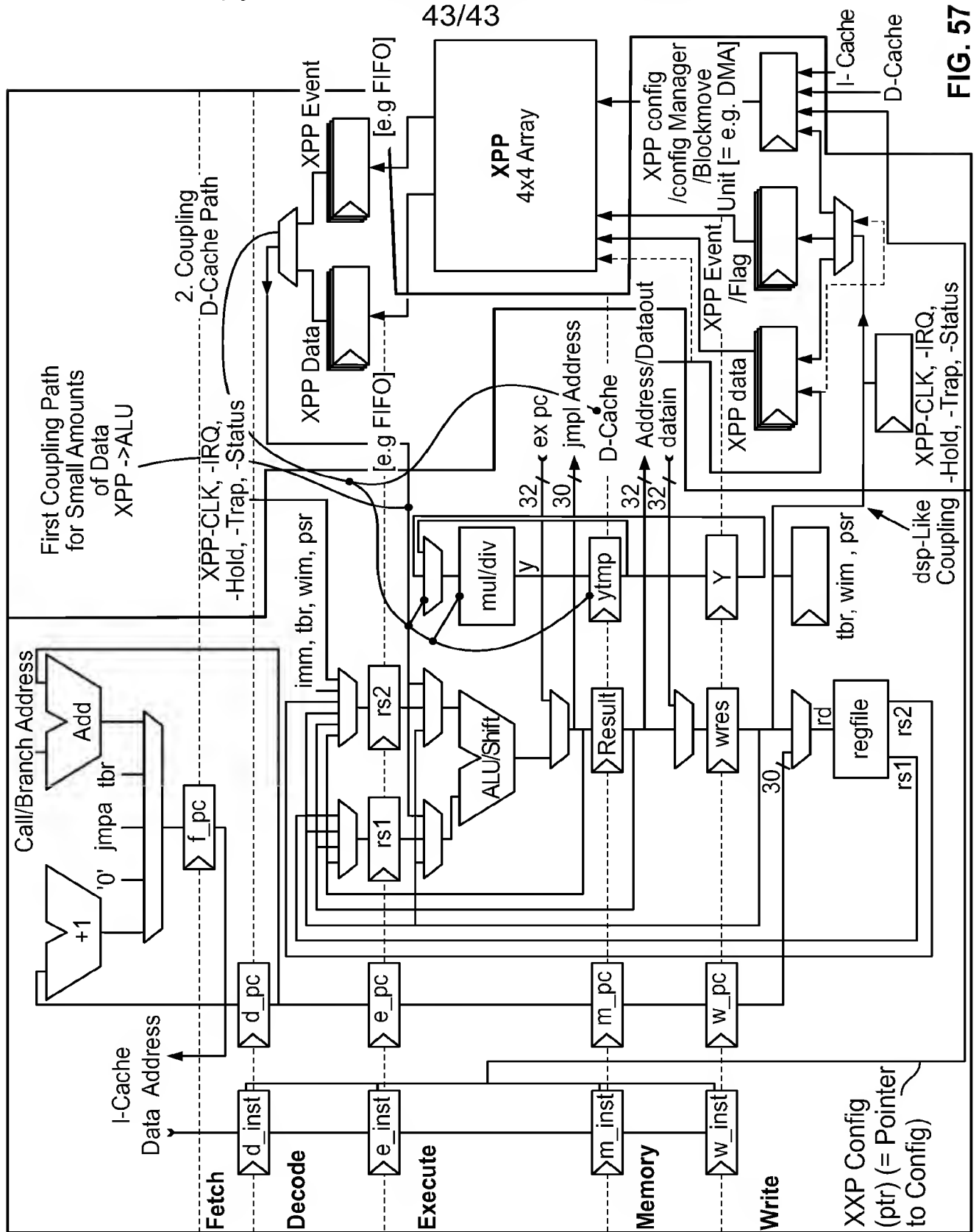


FIG. 57